

Compal confidential

HGT30/31 Schematics Document

Mobile Yonah uFCPGA with Intel
Calistoga_GM/PM+ICH7-M core logic

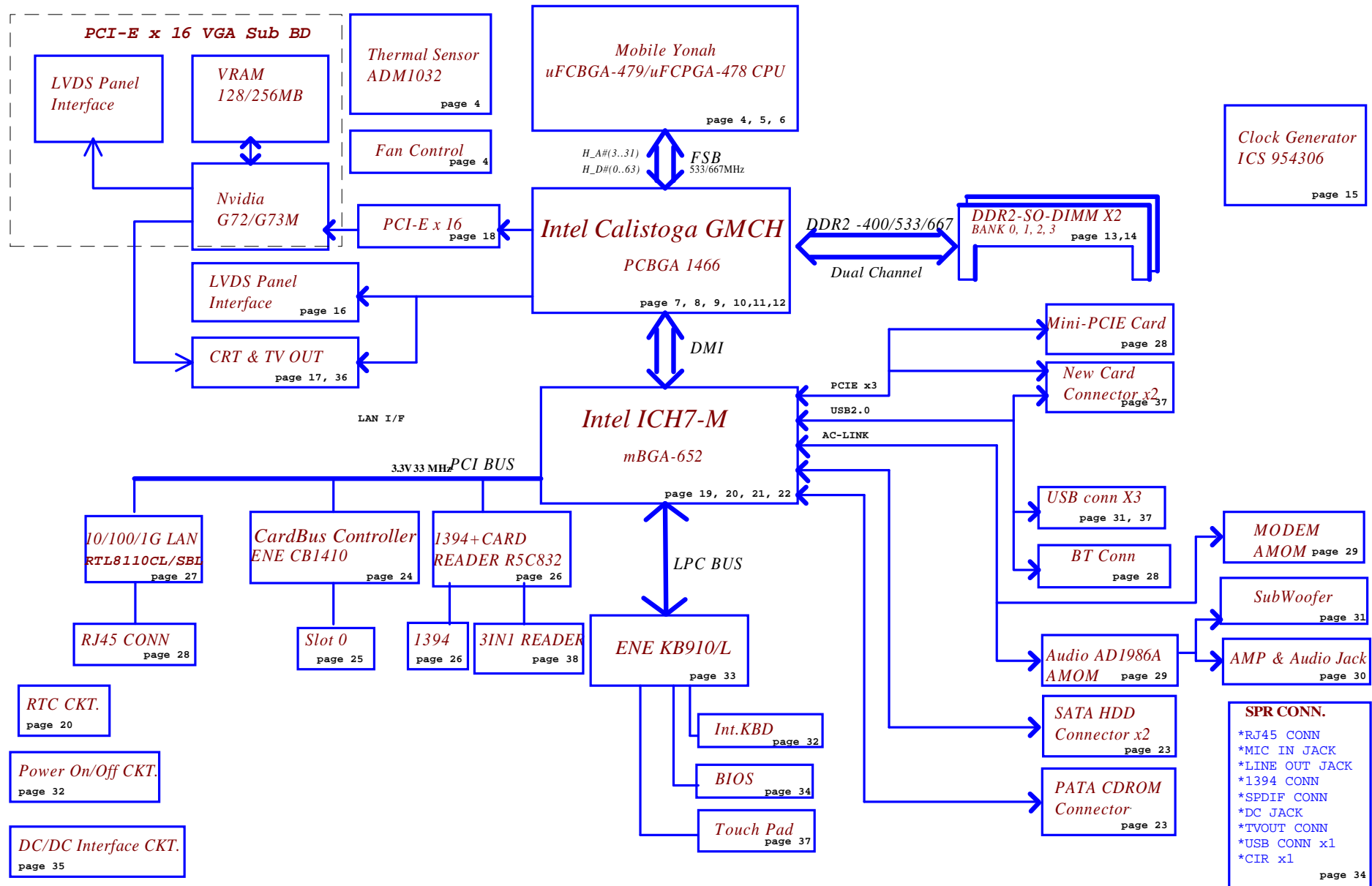
2006-02-15

REV:0.3

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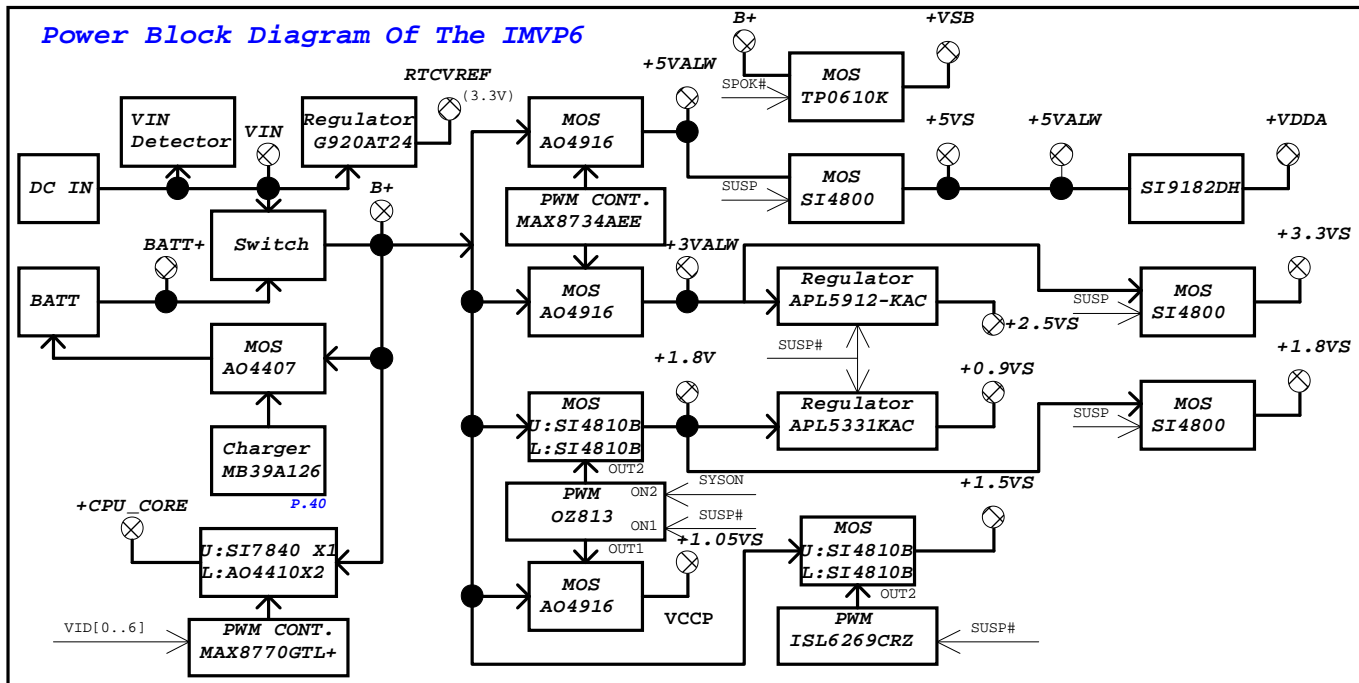
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File Name : LA-3061



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Power Block Diagram Of The IMVP6



PCI DEVICES

EXTERNAL	IDSEL#	REQ/GNT#	PIRQ
CARD BUS CB1410	AD20	2	PCI_PIRQA#
CARD READER & 1394 R5C832	AD22	0	PCI_PIRQ# PCI_PIRQH#
LAN CONTROLLER RTL8110SBL/CL	AD17	3	PCI_PIRQF#

PCIE LANE

LANE	DEVICE
1	Express Card
2	Mini Card

USB

PORT	DEVICE
0	LEFT SIDE
1	BLUE TOOTH
2	RIGHT SIDE
3	NC
4	RIGHT SIDE
5	NC
6	NC

I2C / SMB Address

DEVICE	ADDRESS R/W
KB910/L (SM1-Pulled-Up 5V)	
AT24C16AN	A3/A2 H
SMART BATTERY	17/16 H
KB910/L (SM2-Pulled-Up 3.3V)	
ADM1032AR	99/98 H
G7xM (I2CC-Pulled-Up 3.3V)	
G781-1 (RESERVED)	9B/9A
ICH7M SM Bus	
ICS9LPR325AKLFT	D3/D2 H (3.3V)
DDR II DIMM0	A1/A0 H (3.3V)
DDR II DIMM1	A3/A2 H (3.3V)
Express Card	NC (2.5V)
Mini-Express	NC (2.5V)

BOM Structure

MARK	FUNCTION
@	NC FOR ALL
EXP@	PCIE-NEW CARD
BT@	BLUE TOOTH
UMA@	Internal 945GM
VGA@	External G7xM
SUBWOOFER@	SUBWOOFER
HGT30@	HGT30
CB@	PCMCIA/CARD BUS
GIGA@	8110SBL (SCL) Giga LAN
10/100@	8110CL 10/100Mb LAN

Voltage Rails

power plane	+B LDO3 LDO5	+5VALW +3VALW	+1.8V +5V	+5VS +3VS +2.5VS +1.8VS +1.5VS +VGA CORE +1.2VS +0.9VS +CPU CORE +VCCP
State S4 : STD S5 : SOFT OFF				
S0	O	O	O	O
S1	O	O	O	O
S3 : STR	O	O	O	X
S5 S4/AC	O	O	X	X
S5 S4/ Battery only	O	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

MB_ID

MB ID	P NAME
0	HGT-30
1	HGT-31

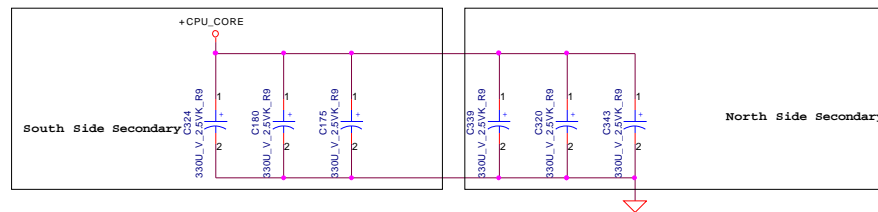
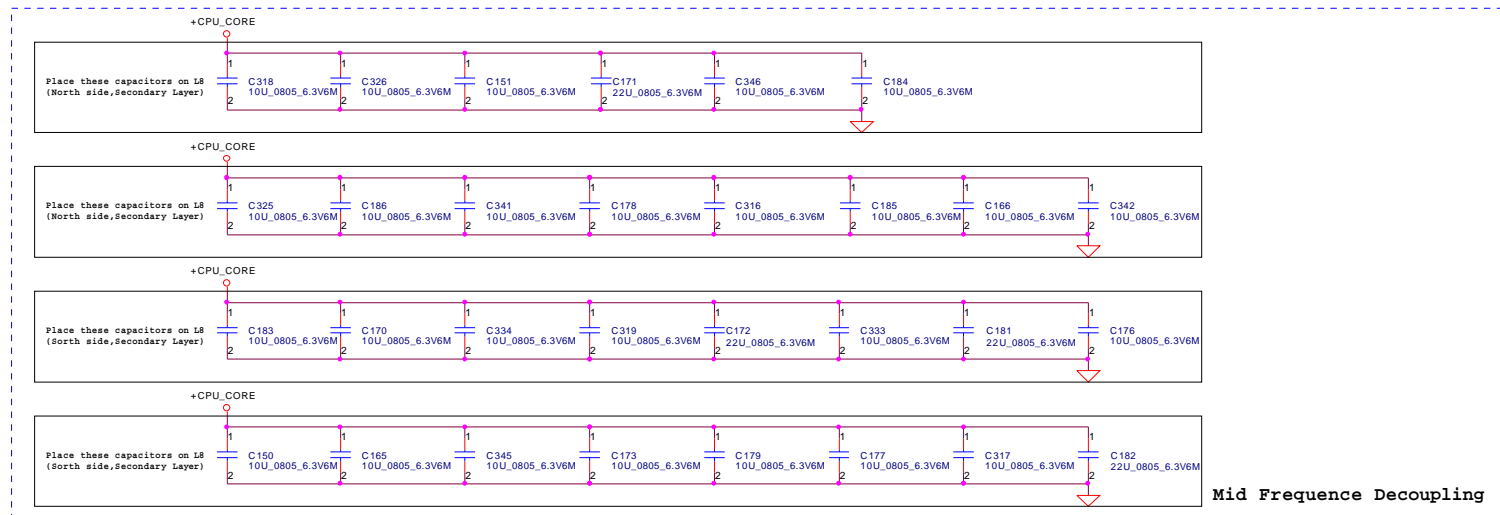
BRD_ID

R119(Ra)=100K Ohm

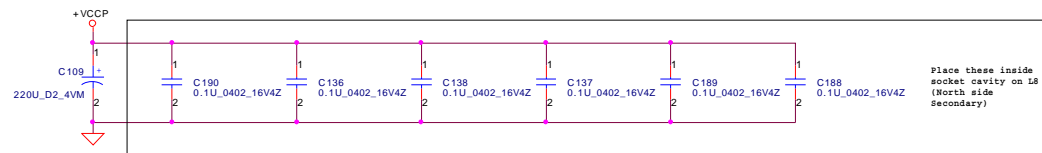
ID	MB REV#	R115(Rb)	Vab
0	R01 (EVT)	0	0V
1	R02 (DVT)	8.2K	0.25V
2	R03	18K	0.50V
3	R04	33K	0.82V
4		56K	1.19V
5		100K	1.65V
6		200K	2.20V
7		NC	3.30V

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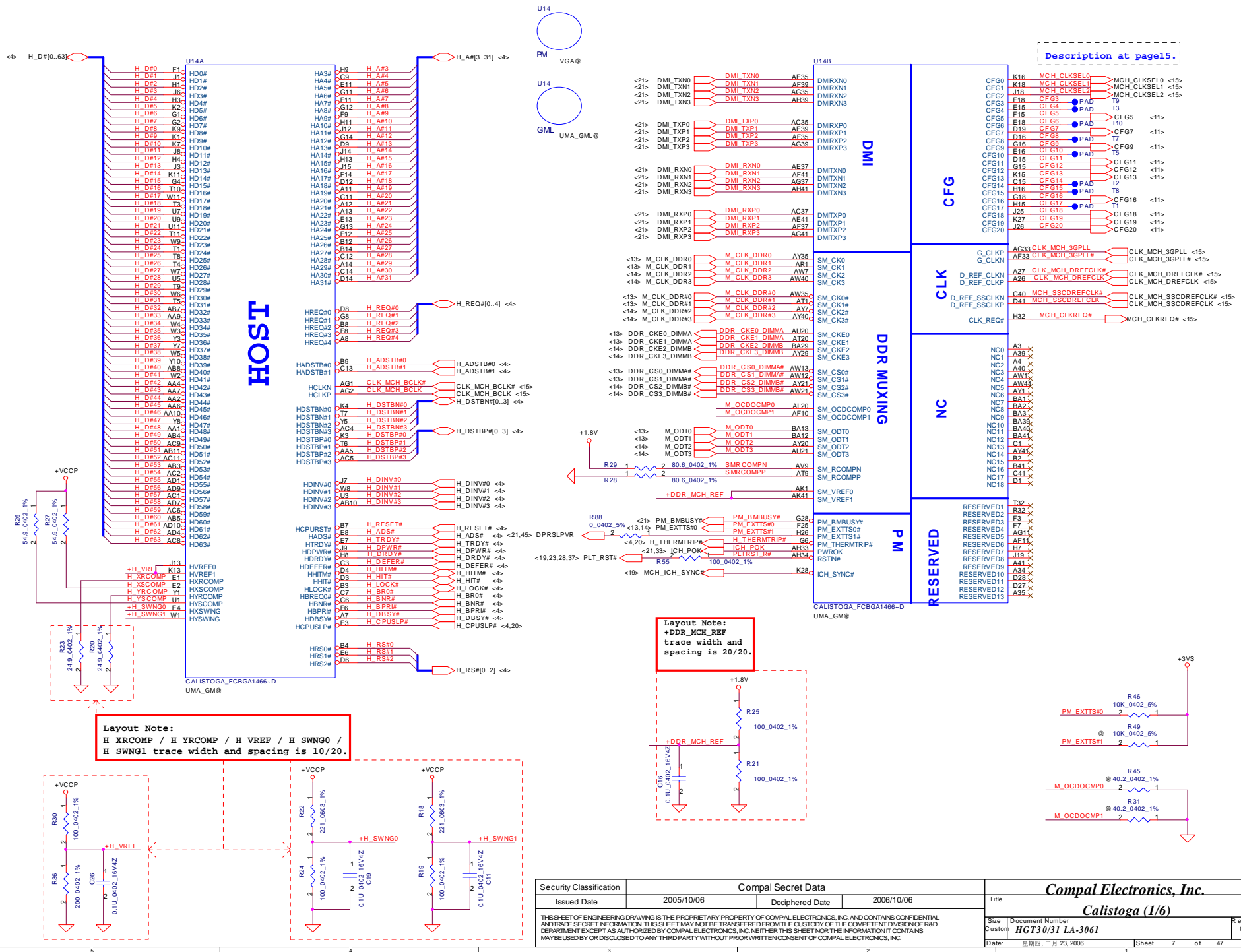
+3VALW	KB910L SB RTL8110SBL/CL	mA 160mA
+CPU_CORE	CPU	36A
+VCCP	CPU NB	2.5A 9.8A (14.7A)
+5VS	EXPRESS CARD HDD ODD MDC APA2066 TPA0211 AD1986 USB PORT * 6	1A 1.5A 1.8A 300mA 1A mA 70mA 3A
+3VS	NB EXPRESS CARD CLK_GEN LCDVCC VGA CARD (G7XM) SB R5C832 BIOS ROM KB910L CB1410	480mA 1A 200mA 1A 655mA 680mA mA 15mA 200mA mA
+2.5VS	VGA CARD (G7XM) NB	130mA (143mA)
+1.8V	DDR2_DIMM NB (667Mhz)	8A 3.1A
+1.8VS	GDDR2 VGA CARD (G7XM)	6A 4.06A
+0.9VREF	DDR2_DIMM	10mA
+0.9VS	GDDR2 DDR2_DIMM	1A 2A
+1.5V	SB	40mA
+1.5VS	NB SB MiniCard EXPRESS CARD VGA CARD (G7XM)	8.9A (13.8A) 3.8A 1A 0.65A 2A

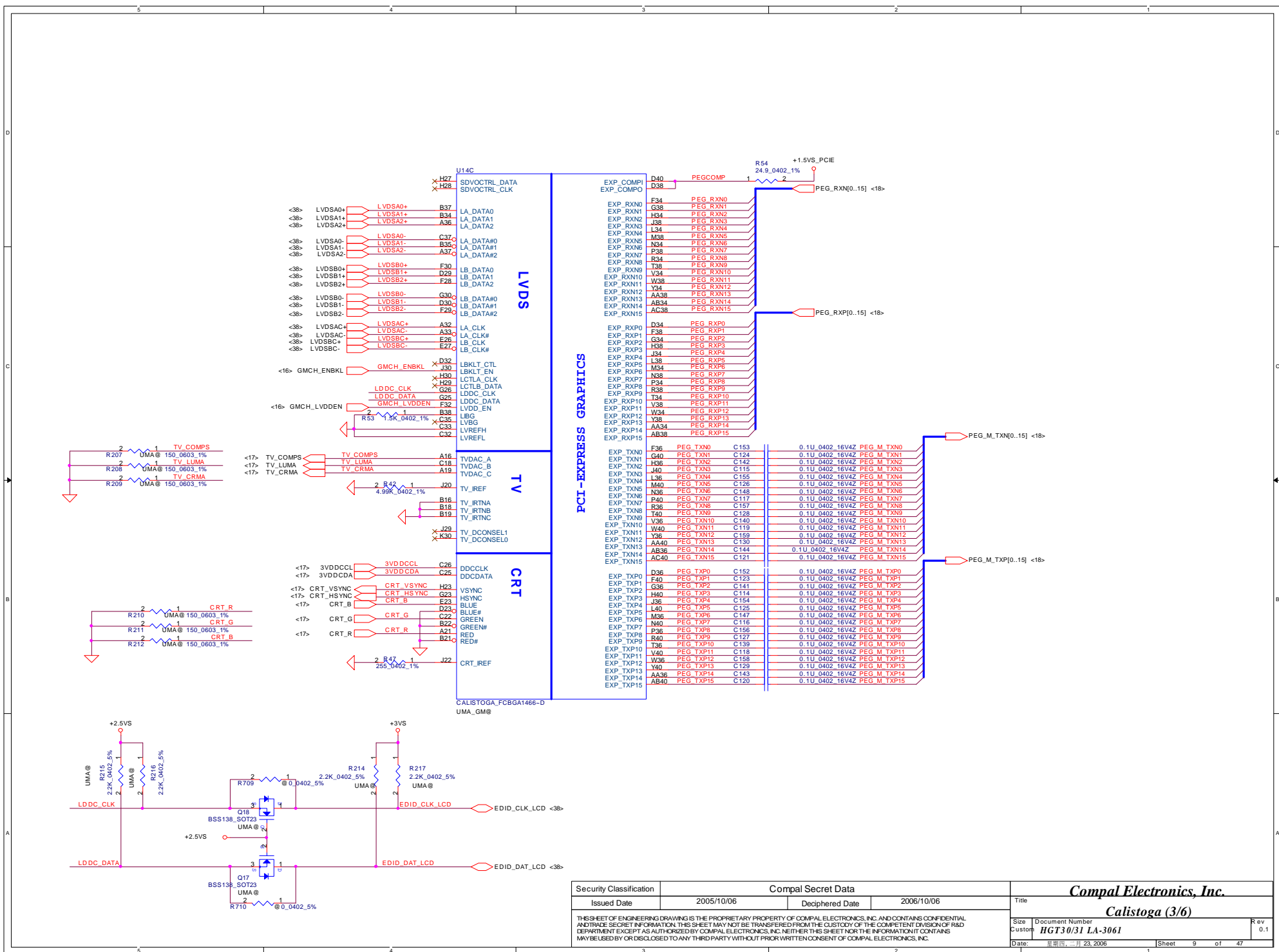


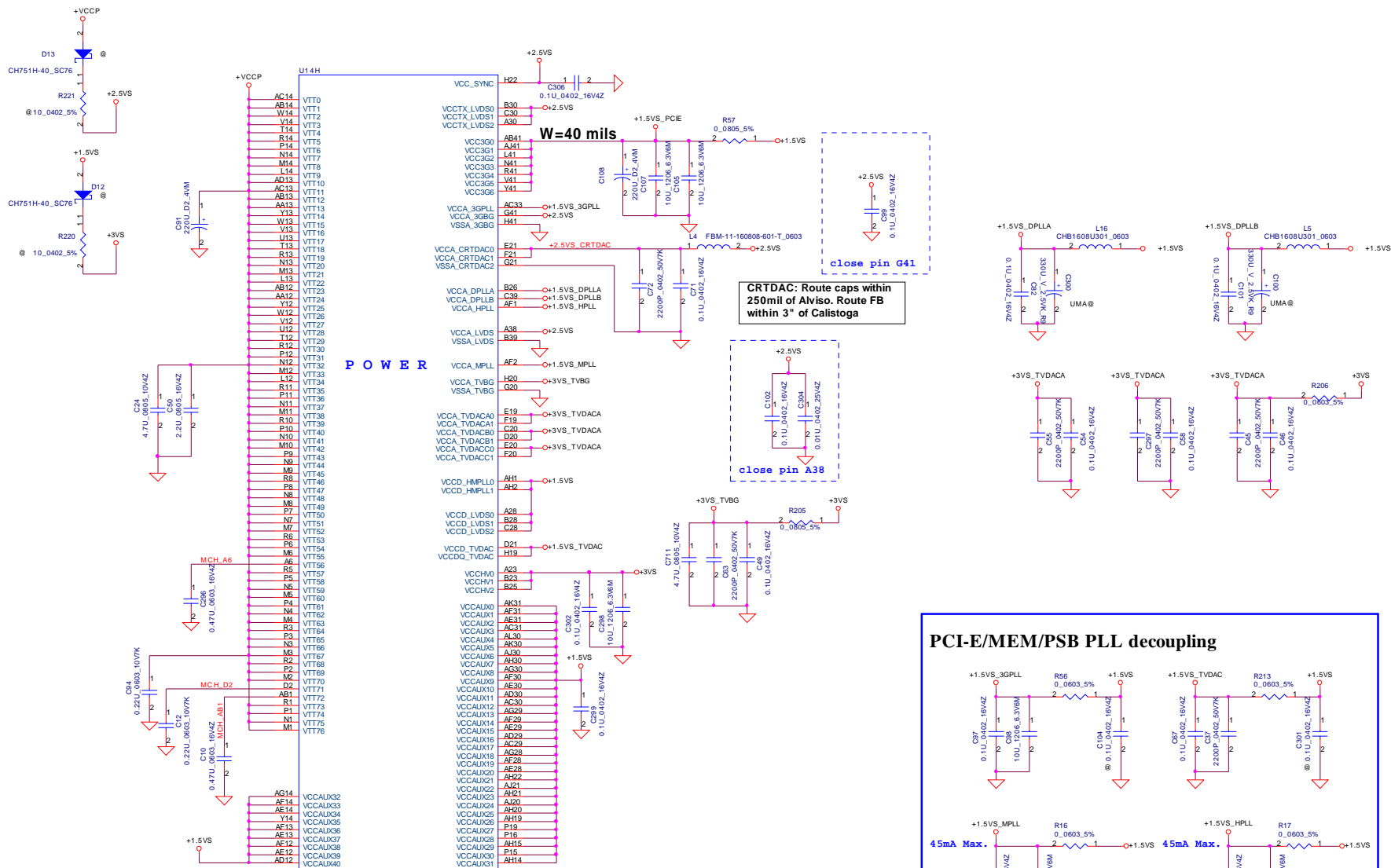
ESR $\leq 1.5m\ \Omega$
Capacitor $> 1980\mu F$



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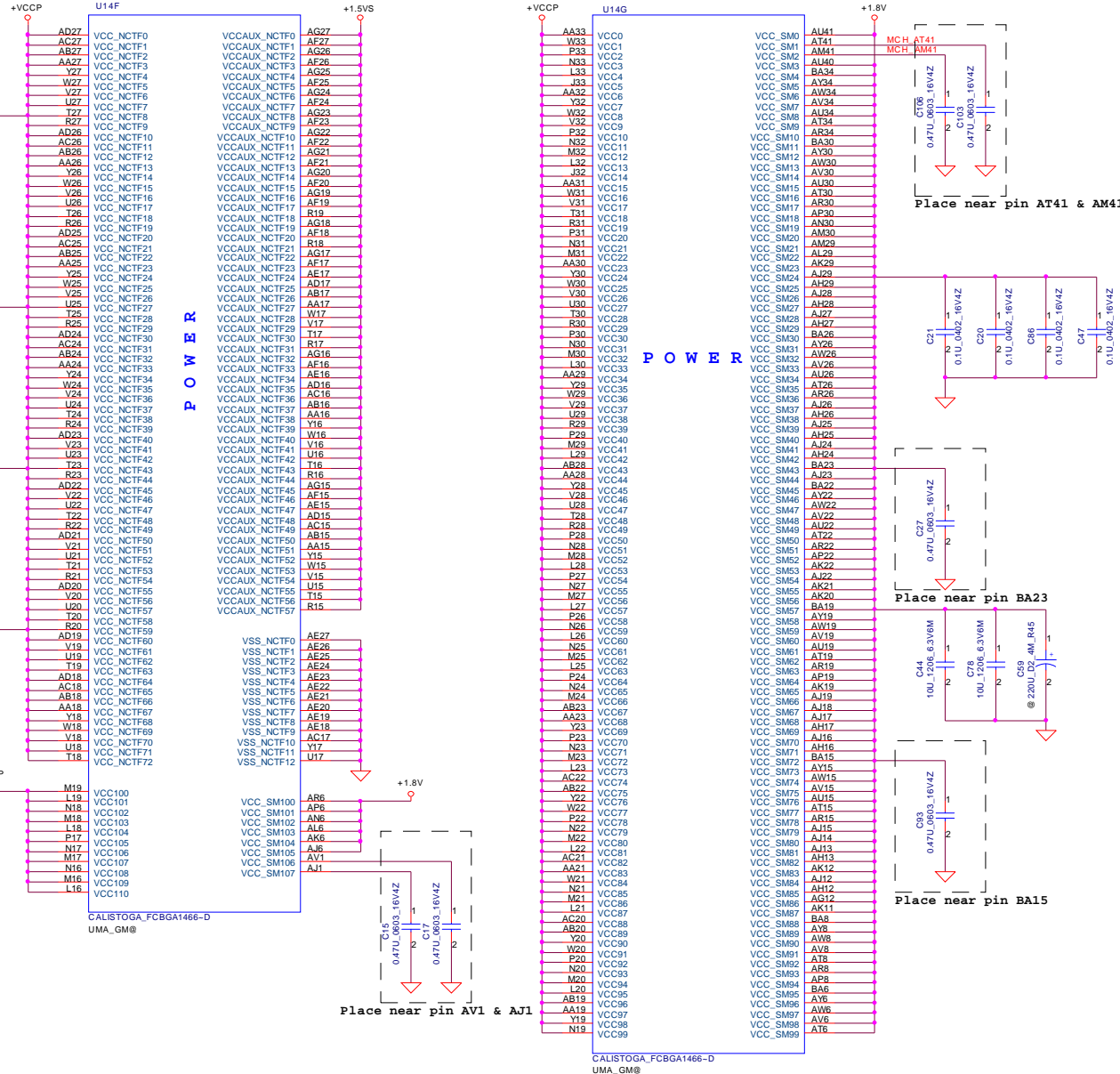
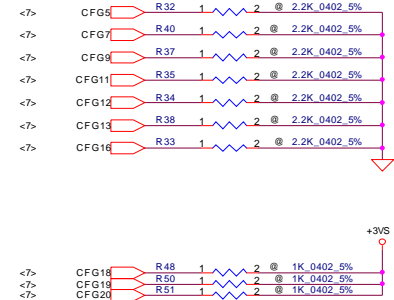
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Strap Pin Table

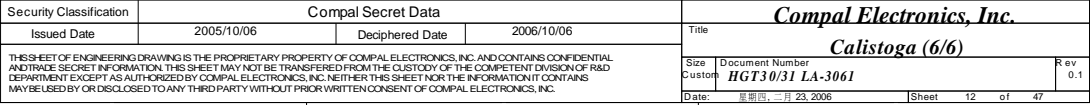
CFG[3:17] have internal pull up

CFG[19:18] have internal pull down

CFG[2:0]	011 = 667MT/s FSB 001 = 533MT/s FSB
CFG5	0 = DMI x 2 1 = DMI x 4 * (Default)
CFG7	0 = Reserved 1 = Mobile Yonah CPU * (Default)
CFG9	0 = Lane Reversal Enable 1 = Normal Operation (Default) *
CFG6	0 = Reserved
PSB 4X CLK Enable	1 = Calistoga *
CFG[13:12]	00 = Reserved 01 = XOR Mode Enabled 10 = All Z Mode Enabled 11 = Normal Operation * (Default)
CFG16	0 = Dynamic ODT Disabled 1 = Dynamic ODT Enabled * (Default)
CFG10 CFG18	10 = 1.05V * (Default) 01 = 1.5V
CFG19	0 = Normal Operation * (Default) 1 = DMI Lane Reversal Enable
SDVO_CTRLDATA	0 = No SDVO Device Present * (Default) 1 = SDVO Device Present
CFG20 (PCIE/SDVO select)	0 = Only PCIE or SDVO is operational. * (Default) 1 = PCIE/SDVO are operating simu.

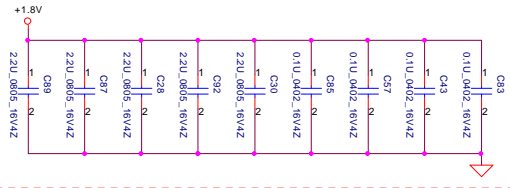


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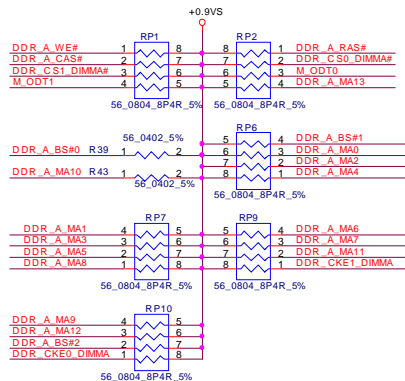
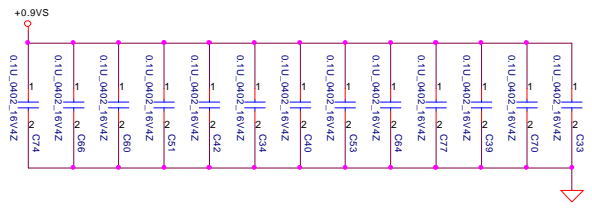


<8> DDR_A_DQS#0[0..7]
 <8> DDR_A_D[0..63]
 <8> DDR_A_DM[0..7]
 <8> DDR_A_DQS[0..7]
 <8> DDR_A_MA[0..13]

Layout Note:
Place near JP41

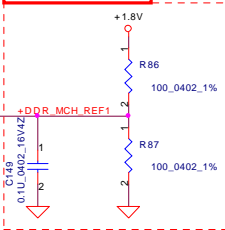


Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9VS



Layout Note:
Place these resistor closely JP41, all trace length Max<1.5"

Layout Note:
+DDR_MCH_REF trace width and spacing is 20/20.



<14,33> EC_P80_DATA

<7> DDR_CKE0_DIMMA

<14,33> EC_P80_CLK

<8> DDR_A_BS#2

<8> DDR_A_BS#0

<8> DDR_A_WE#

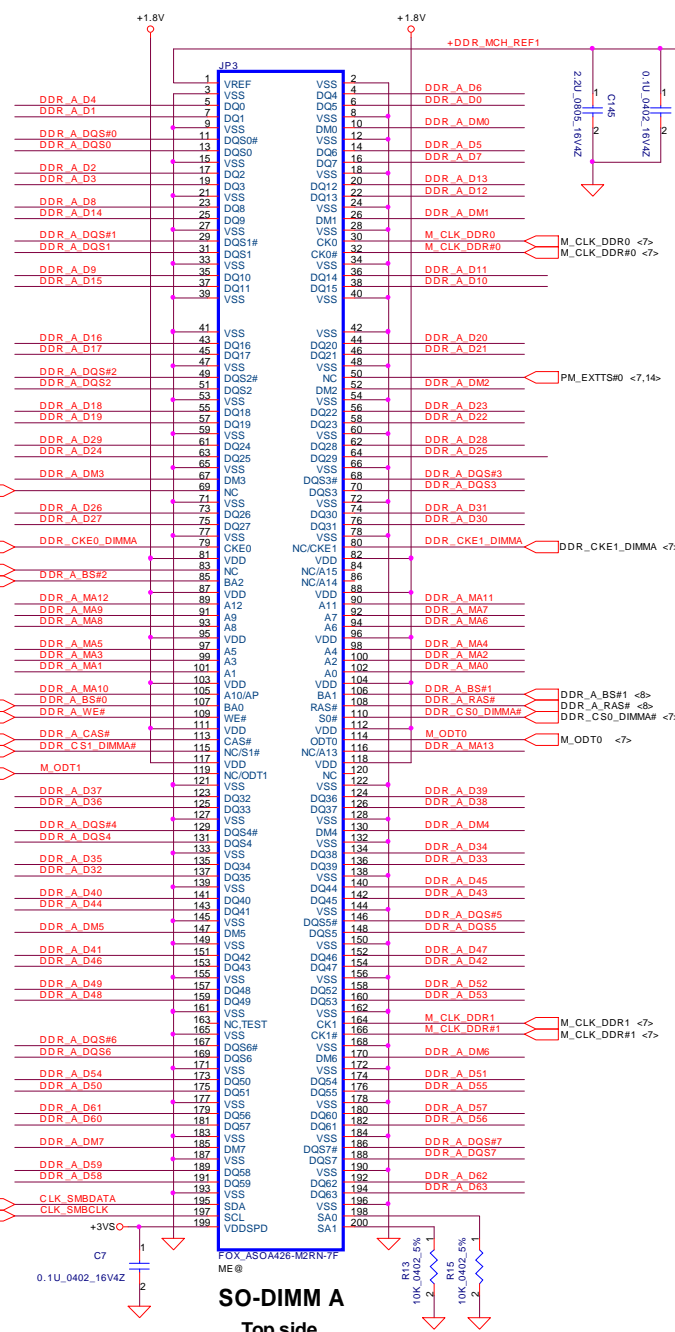
<8> DDR_A_CAS#

<7> DDR_CS1_DIMMA#

<7> M_ODT1

<14,15> CLK_SMBDATA

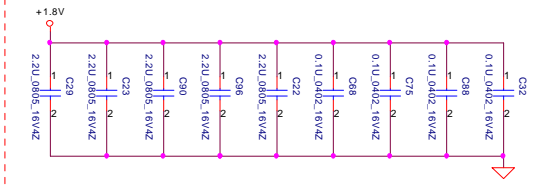
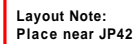
<14,15> CLK_SMBCLK



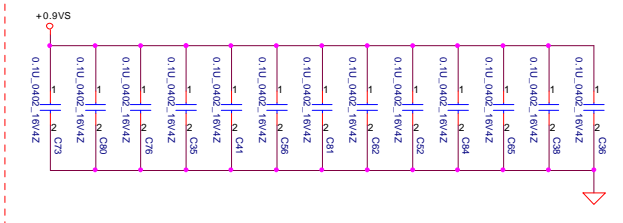
SO-DIMM A

Top side

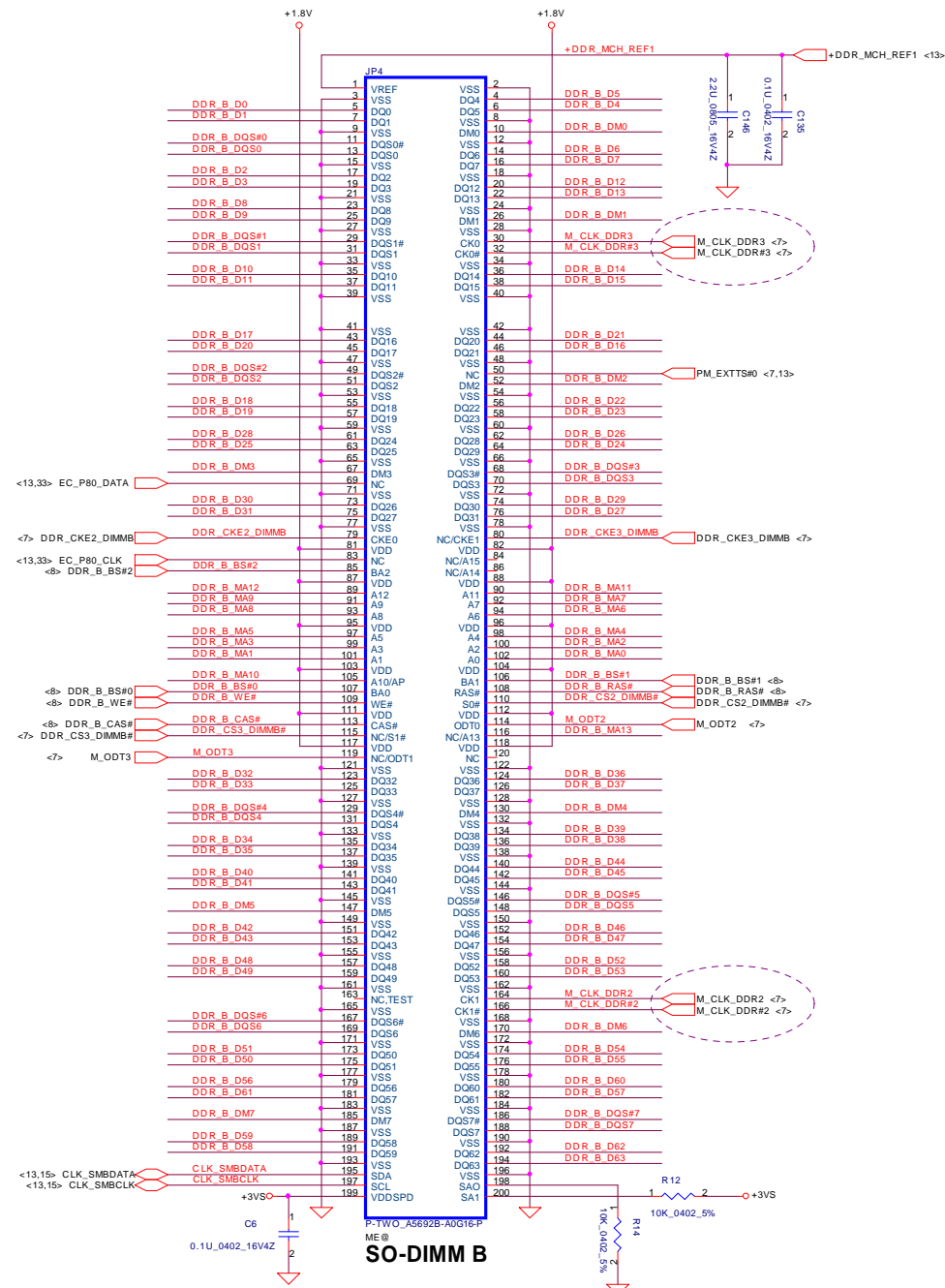
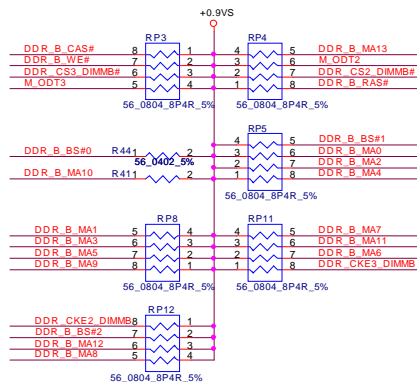
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					DDRII-SODIMM SLOT1
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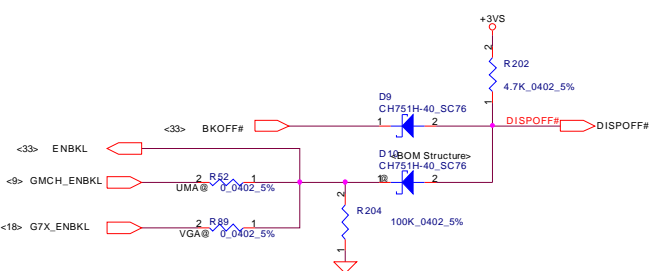
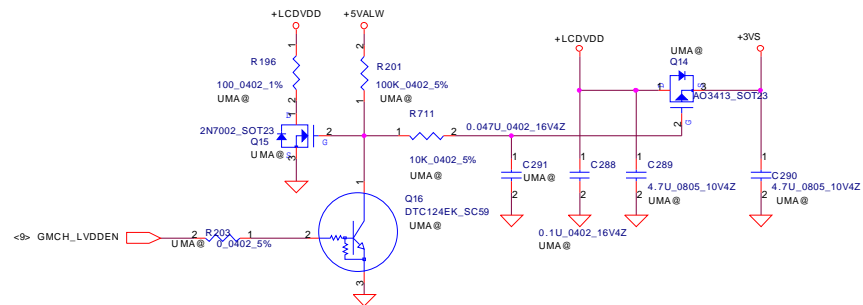
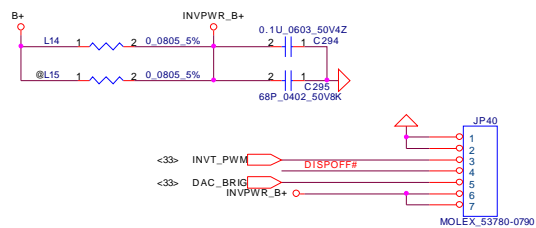
Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9VS



Layout Note:
Place these resistor
closely JP42,all
trace length Max=1.5"



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LVDS Connector

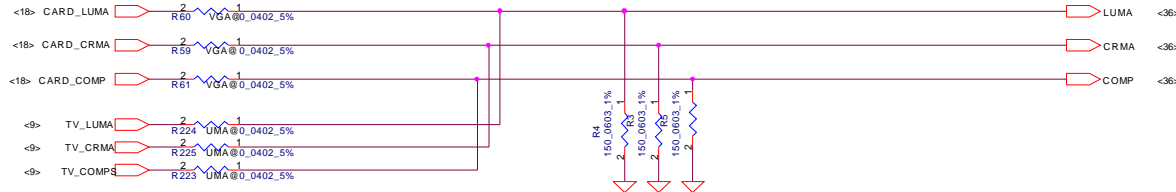
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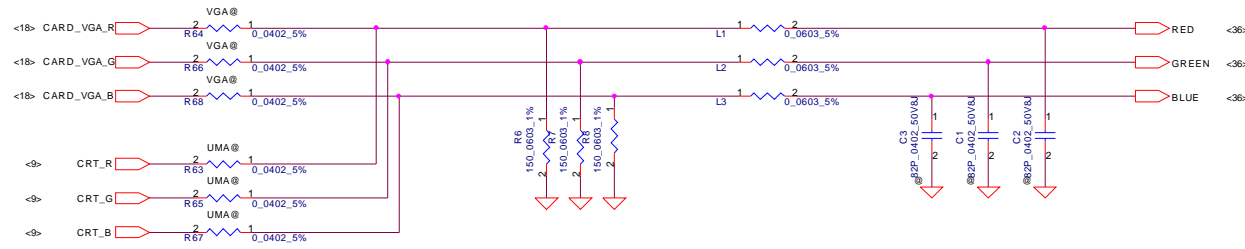
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TV-OUT Conn.

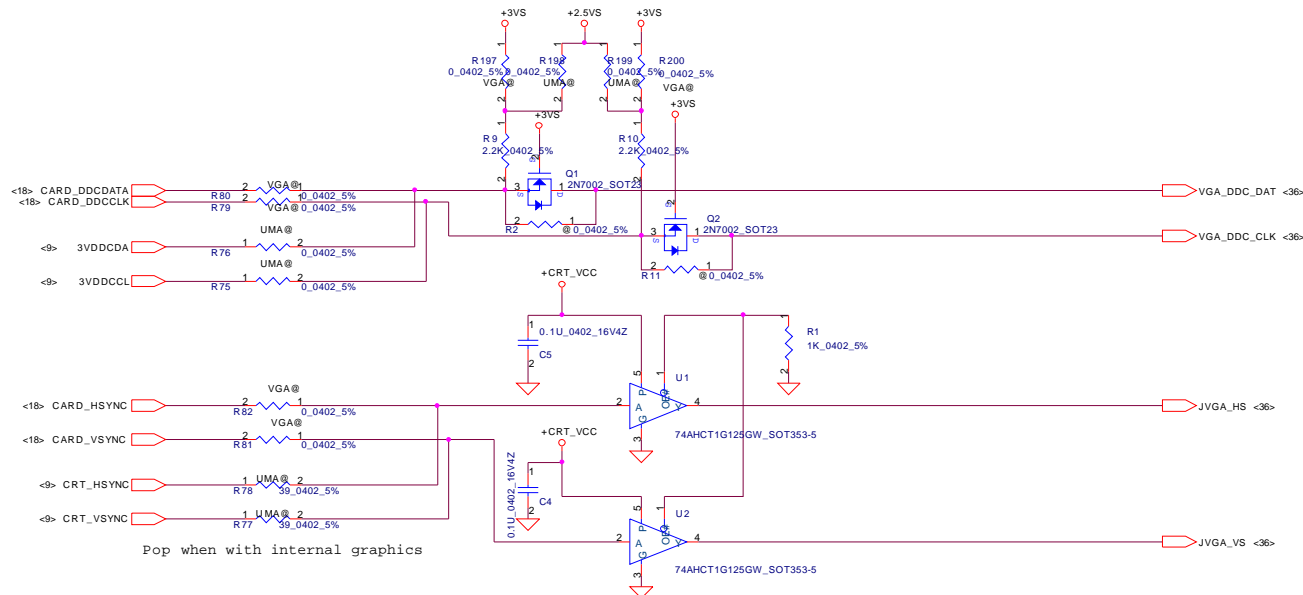


Pop when with internal graphics

CRT Conn.

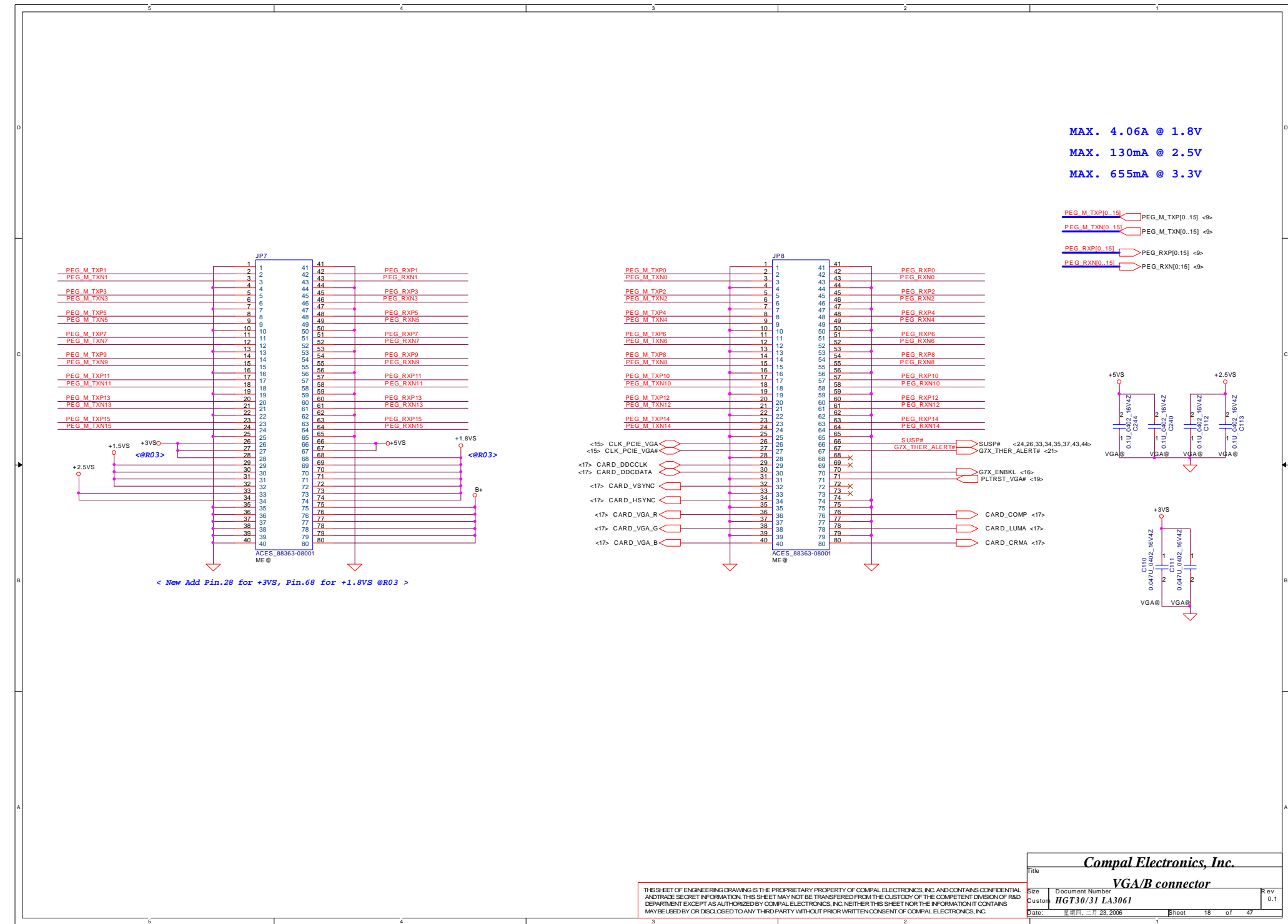


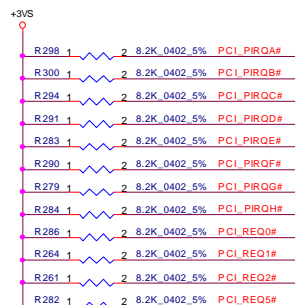
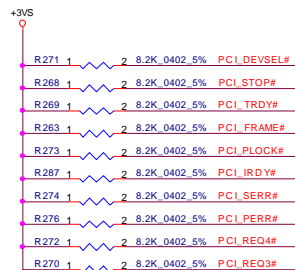
Pop when with internal graphics



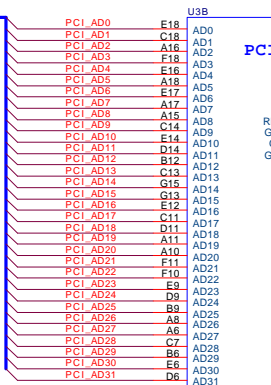
Pop when with internal graphics

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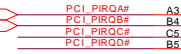




<24,26,27,32> PCI_AD[0..31]



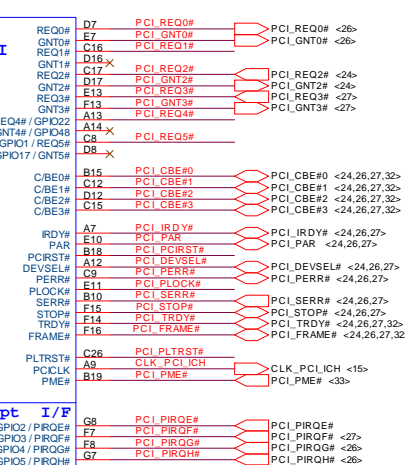
<24> PCI_PIRQA#
PCI_PIRQB#



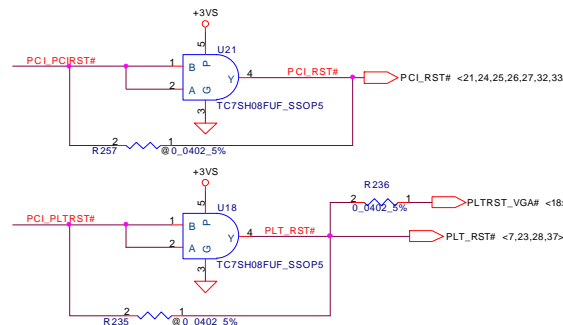
Interrupt I/F
GPIO2 / IRQA#
GPIO3 / IRQB#
GPIO4 / IRQC#
GPIO5 / IRQD#

MISC
RSVD[1]
RSVD[2]
RSVD[3]
RSVD[4]
RSVD[5]
MCH_SYNC#

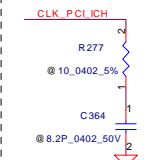
ICH7_BGA852-D



MISC
RSVD[6]
RSVD[7]
RSVD[8]
RSVD[9]
MCH_SYNC#

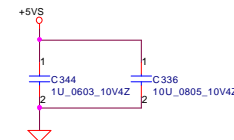
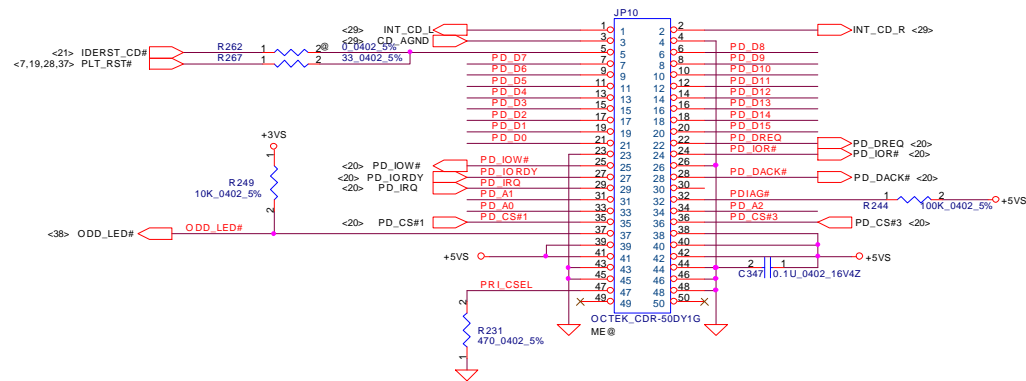
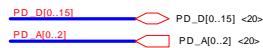
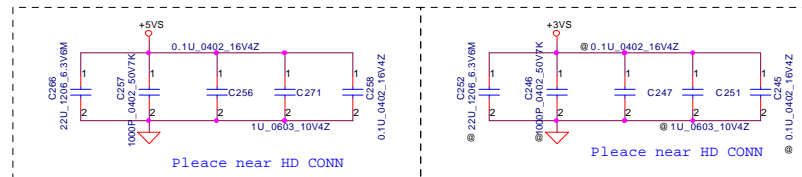
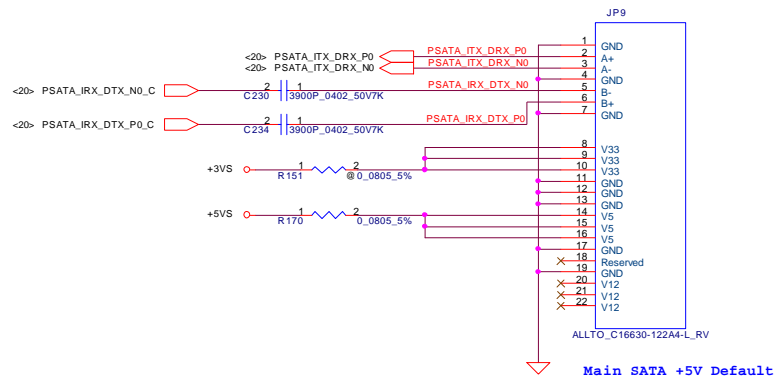


Place closely pin A9



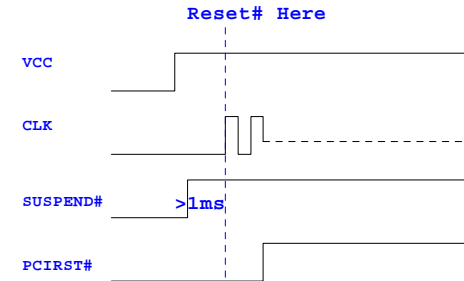
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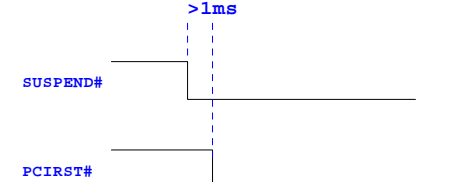


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				Custom	HGT30/31 LA3061
				Date:	日期: 二月 23, 2006
				Sheet	23 of 47
				Rev	0.1

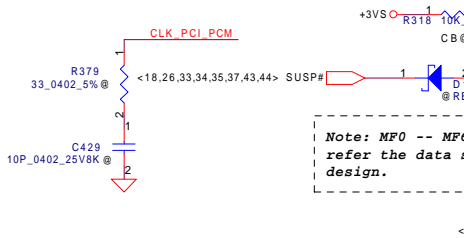
Power on RESET#



Entry S3



SUSPEND# will gate the PCIRST# or GRST#, so need S3 wake up function, SUSPEND# must be LOW ahead the PCIRST# about 1ms.



CARD_S1_A[0..25] <25>
CARD_S1_D[0..15] <25>
PCI_AD[0..31] <19,26,27,32>

<25> VPPD0
<25> VPPD1
<25> VCCD0#
<25> VCCD1#

PCI_AD31 3 AD31
PCI_AD30 4 AD30
PCI_AD29 5 AD29
PCI_AD28 7 AD28
PCI_AD27 8 AD27
PCI_AD26 9 AD26
PCI_AD25 10 AD25
PCI_AD24 11 AD24
PCI_AD23 15 AD23
PCI_AD22 16 AD22
PCI_AD21 17 AD21
PCI_AD20 19 AD20
PCI_AD19 23 AD19
PCI_AD18 24 AD18
PCI_AD17 25 AD17
PCI_AD16 26 AD16
PCI_AD15 38 AD15
PCI_AD14 39 AD14
PCI_AD13 40 AD13
PCI_AD12 41 AD12
PCI_AD11 43 AD11
PCI_AD10 45 AD10
PCI_AD9 46 AD9
PCI_AD8 47 AD8
PCI_AD7 49 AD7
PCI_AD6 51 AD6
PCI_AD5 52 AD5
PCI_AD4 53 AD4
PCI_AD3 54 AD3
PCI_AD2 55 AD2
PCI_AD1 56 AD1
PCI_AD0 57 AD0

<19,26,27,32> PCI_CBE#3
<19,26,27,32> PCI_CBE#2
<19,26,27,32> PCI_CBE#1
<19,26,27,32> PCI_CBE#0

<19,21,25,26,27,32,33> PCI_RST#
<19,26,27,32> PCI_FRAME#
<19,26,27> PCI_TRDY#
<19,26,27,32> PCI_TRDY#
<19,26,27> PCI_DEVSEL#
<19,26,27> PCI_STOP#
<19,26,27> PCI_PERR#
<19,26,27> PCI_SERR#
<19,26,27> PCI_PAR#
<19> PCI_REQ2#
<19> PCI_GNT2#
<15> CLK_PCI_PCM

<19,26,27,32,33> PCI_RST#
<19> PCI_PIRQA#
<21,26,32,33> SIRQ
<21,26,27,33> PCI_CLKRUN#
<19,21,25,26,27,32,33> PCI_RST#

PQFP 144
22.2 X 22.2 X 1.60

PCI_CBE#3 12 C/BE3#
PCI_CBE#2 27 C/BE2#
PCI_CBE#1 37 C/BE1#
PCI_CBE#0 48 C/BE0#

PCI_RST# 20 RST#
PCI_FRAME# 28 FRAME#
PCI_TRDY# 29 TRDY#
PCI_TRDY# 31 TRDY#
PCI_DEVSEL# 32 DEVSEL#
PCI_STOP# 33 STOP#
PCI_PERR# 34 PERR#
PCI_SERR# 35 SERR#
PCI_PAR# 36 PAR#
PCI_REQ2# 37 REQ#
PCI_GNT2# 38 GNT#
CLK_PCI_PCM 21 CLK

PCI_AD20 1 PCI_PIRQA# 60
PCI_PIRQA# 61 MFUNC0
SIRQ 62 MFUNC1
SIRQ 63 MFUNC2
SIRQ 64 MFUNC3
SIRQ 65 MFUNC4
SIRQ 66 MFUNC5
SIRQ 67 MFUNC6
SIRQ 68
SIRQ 69
SIRQ 70

RI_OUT#/PME# 59
SUSPEND# 70
IDSEL 71
MFUNC0 60
MFUNC1 61
MFUNC2 62
MFUNC3 63
MFUNC4 64
MFUNC5 65
MFUNC6 66
VCC/GRST# 67
GND1 68
GND2 69
GND3 70
GND4 71
GND5 72
GND6 73
GND7 74
GND8 75
GND9 76
GND10 77
GND11 78
GND12 79
GND13 80
GND14 81
GND15 82
GND16 83
GND17 84
GND18 85
GND19 86
GND20 87
GND21 88
GND22 89
GND23 90
GND24 91
GND25 92
GND26 93
GND27 94
GND28 95
GND29 96
GND30 97
GND31 98
GND32 99
GND33 100
GND34 101
GND35 102
GND36 103
GND37 104
GND38 105
GND39 106
GND40 107
GND41 108
GND42 109
GND43 110
GND44 111
GND45 112
GND46 113
GND47 114
GND48 115
GND49 116
GND50 117
GND51 118
GND52 119
GND53 120
GND54 121
GND55 122
GND56 123
GND57 124
GND58 125
GND59 126
GND60 127
GND61 128
GND62 129
GND63 130
GND64 131
GND65 132
GND66 133
GND67 134
GND68 135
GND69 136
GND70 137
GND71 138
GND72 139
GND73 140
GND74 141
GND75 142
GND76 143
GND77 144

ENE CBI410 just have one vcc plane internal, if want S3 wake-up function(PME#), then at S3 status must keep all Vcc +3V. That is different with TI 1410 and O2-Micro 6912, just keep the VCCI pin +3V, the other vcc can use +3VS.

CAD31/D10 144 CARD_S1_D10
CAD30/D9 142 CARD_S1_D9
CAD29/D8 141 CARD_S1_D8
CAD28/D7 140 CARD_S1_D7
CAD27/D6 139 CARD_S1_D6
CAD26/D5 129 CARD_S1_A0
CAD25/A1 128 CARD_S1_A1
CAD24/A2 127 CARD_S1_A2
CAD23/A3 124 CARD_S1_A3
CAD22/A4 121 CARD_S1_A4
CAD21/A5 120 CARD_S1_A5
CAD20/A6 118 CARD_S1_A6
CAD19/A25 116 CARD_S1_A25
CAD18/A7 115 CARD_S1_A7
CAD17/A24 98 CARD_S1_A17
CAD16/A17 96 CARD_S1_IOWR#
CAD15/IORD# 97 CARD_S1_IOR#
CAD14/A9 93 CARD_S1_IORD#
CAD13/IORD# 95 CARD_S1_A11
CAD12/A11 92 CARD_S1_OE#
CAD11/OE# 91 CARD_S1_CE2#
CAD10/CE2# 89 CARD_S1_A10
CAD9/A10 87 CARD_S1_D15
CAD8/D15 85 CARD_S1_D7
CAD7/D7 82 CARD_S1_D13
CAD6/D13 83 CARD_S1_D6
CAD5/D6 80 CARD_S1_D12
CAD4/D12 81 CARD_S1_D5
CAD3/D5 77 CARD_S1_D11
CAD2/D11 79 CARD_S1_D4
CAD1/D4 76 CARD_S1_D3
CAD0/D3 76 CARD_S1_D3

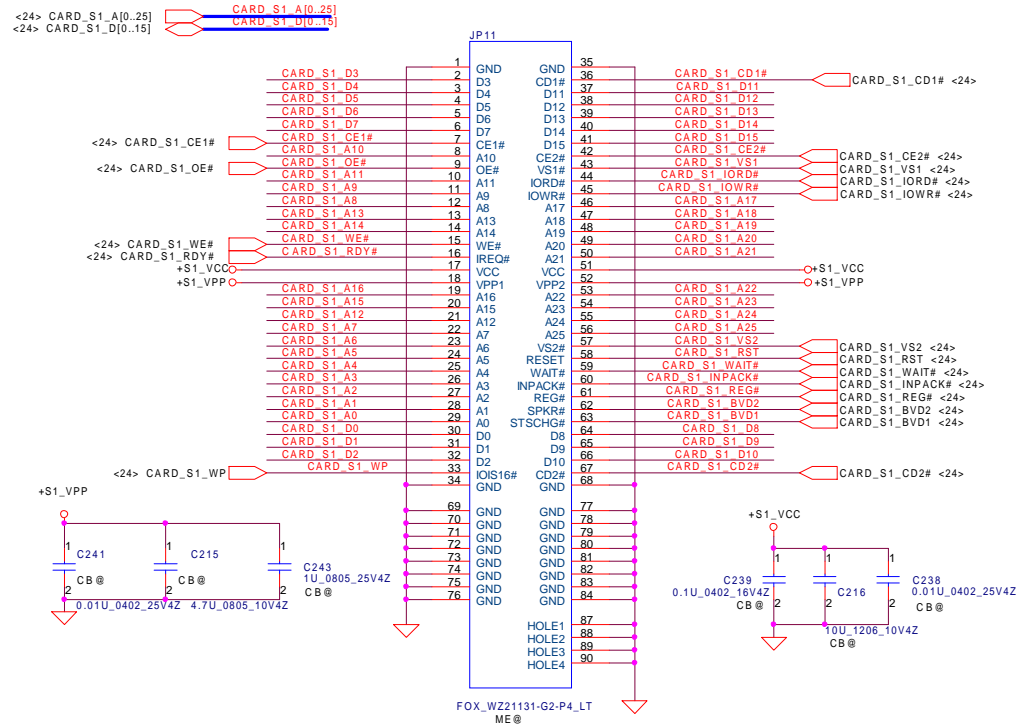
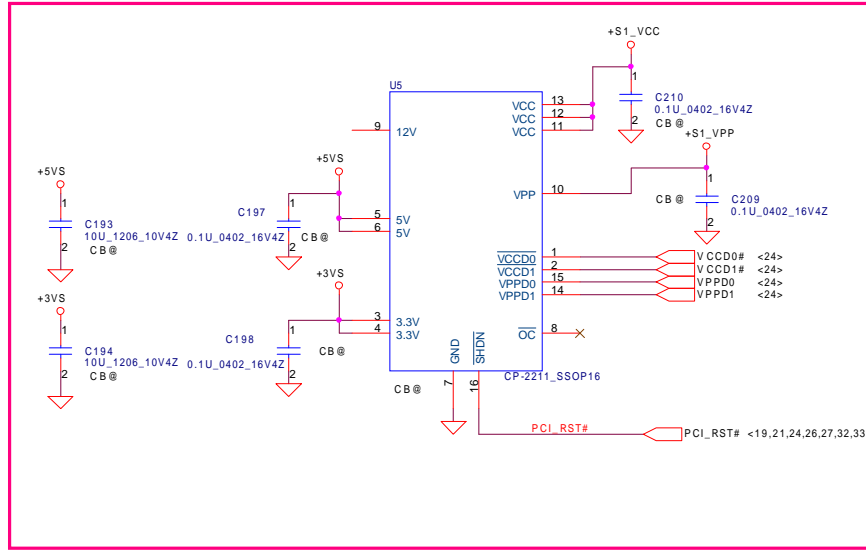
CC/BE3#/REG# 125 CARD_S1_REG#
CC/BE2#/A12 112 CARD_S1_A12
CC/BE1#/A8 99 CARD_S1_A8
CC/BE0#/CE1# 88 CARD_S1_CE1#
CRST#/RESET 119 CARD_S1_RST
CFRAME#/A23 111 CARD_S1_A23
CRDY#/A15 110 CARD_S1_A15
CTRDY#/A22 109 CARD_S1_A22
CDEVSEL#/A21 107 CARD_S1_A21
CSTOP#/A20 105 CARD_S1_A20
CPERR#/A14 104 CARD_S1_A14
CSERR#/WAIT# 133 CARD_S1_WAIT#
CPAR/A13 101 CARD_S1_A13
CREQ#/INPACK# 123 CARD_S1_INPACK#
CGNT#/WE# 108 CARD_S1_WE#
CCLK/A16 108 CARD_S1_A16

CSTSCG/BVD1 135 CARD_S1_BVD1
CCLKRUN#/WP 136 CARD_S1_WP
CBLOCK#/A19 103 CARD_S1_A19
CINT#/READY 132 CARD_S1_RDY#
SPKOUT 62 PCM_SPK#
CAUDIO/BVD2 134 CARD_S1_BVD2
CD2#/CD2# 137 CARD_S1_CD2#
CCD1#/CD1# 137 CARD_S1_CD1#
CVS2/VS2# 131 CARD_S1_VS2
CVS1/VS1# 131 CARD_S1_VS1

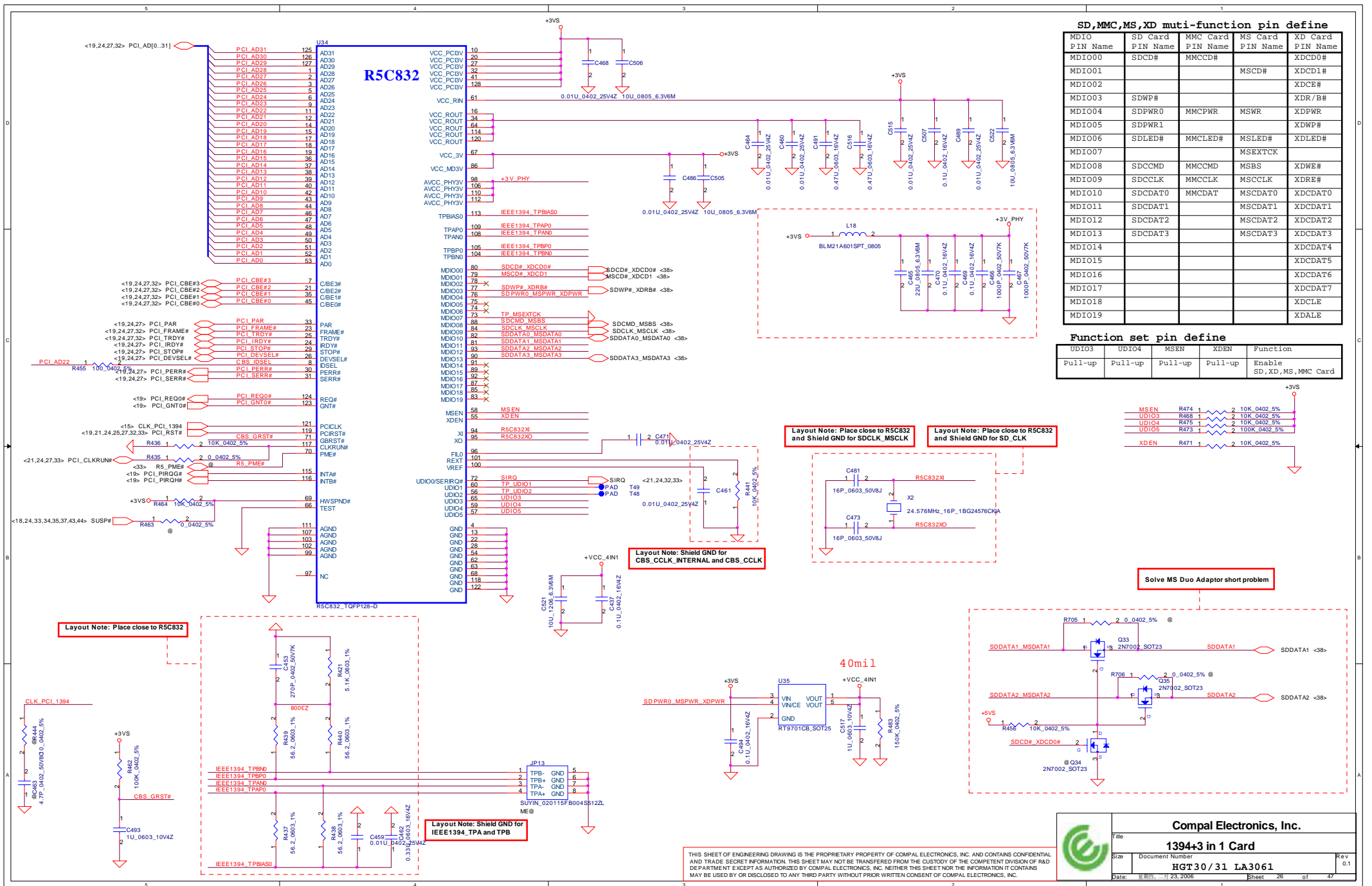
CARD_S1_IOWR# <25>
CARD_S1_IOR# <25>
CARD_S1_OE# <25>
CARD_S1_CE2# <25>
CARD_S1_REG# <25>
CARD_S1_A12 <25>
CARD_S1_A8 <25>
CARD_S1_CE1# <25>
CARD_S1_RST <25>
CARD_S1_A23 <25>
CARD_S1_A15 <25>
CARD_S1_A22 <25>
CARD_S1_A21 <25>
CARD_S1_A20 <25>
CARD_S1_A14 <25>
CARD_S1_WAIT# <25>
CARD_S1_A13 <25>
CARD_S1_INPACK# <25>
CARD_S1_WE# <25>
CARD_S1_A16 <25>
CARD_S1_BVD1 <25>
CARD_S1_WP <25>
CARD_S1_A19 <25>
CARD_S1_RDY# <25>
PCM_SPK# <29>
CARD_S1_BVD2 <25>
CARD_S1_CD2# <25>
CARD_S1_CD1# <25>
CARD_S1_VS2 <25>
CARD_S1_VS1 <25>

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Size		Document Number		Rev	
Date		Date		Sheet	
24		01		47	
0.1		HGT30/31 LA3061		Rev	
Date		Date		Sheet	
24		01		47	
0.1		HGT30/31 LA3061		Rev	

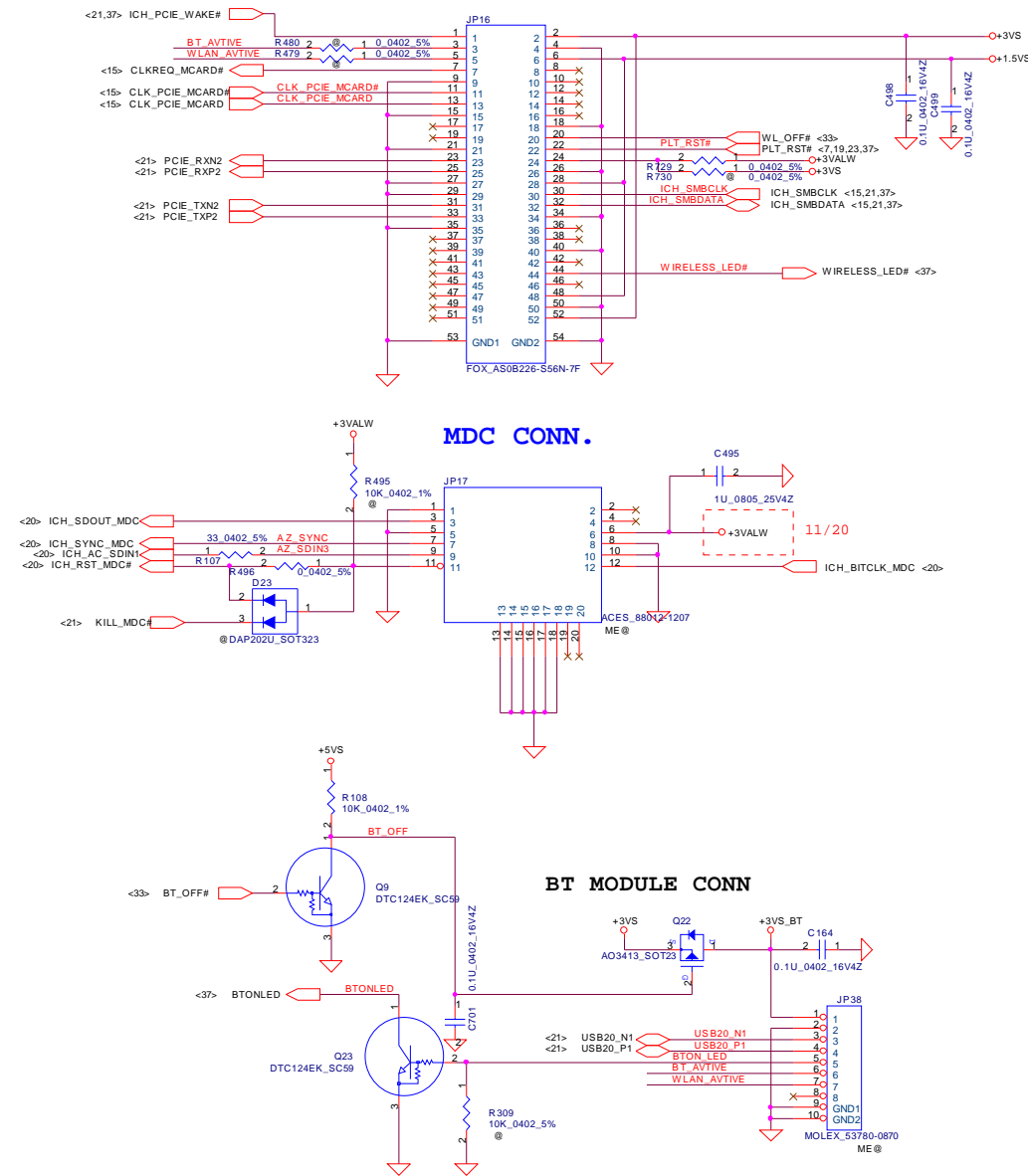
PCMCIA Power Controller



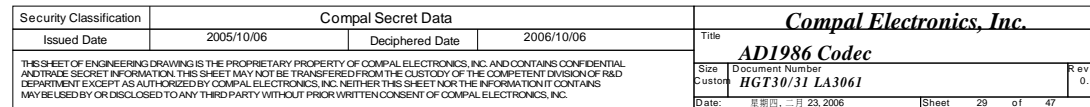
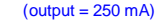
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Size		Document Number		Rev	
Customer		HGT30/31 LA3061		0.1	
Date		日期: 04/23/2006		Sheet 25 of 47	



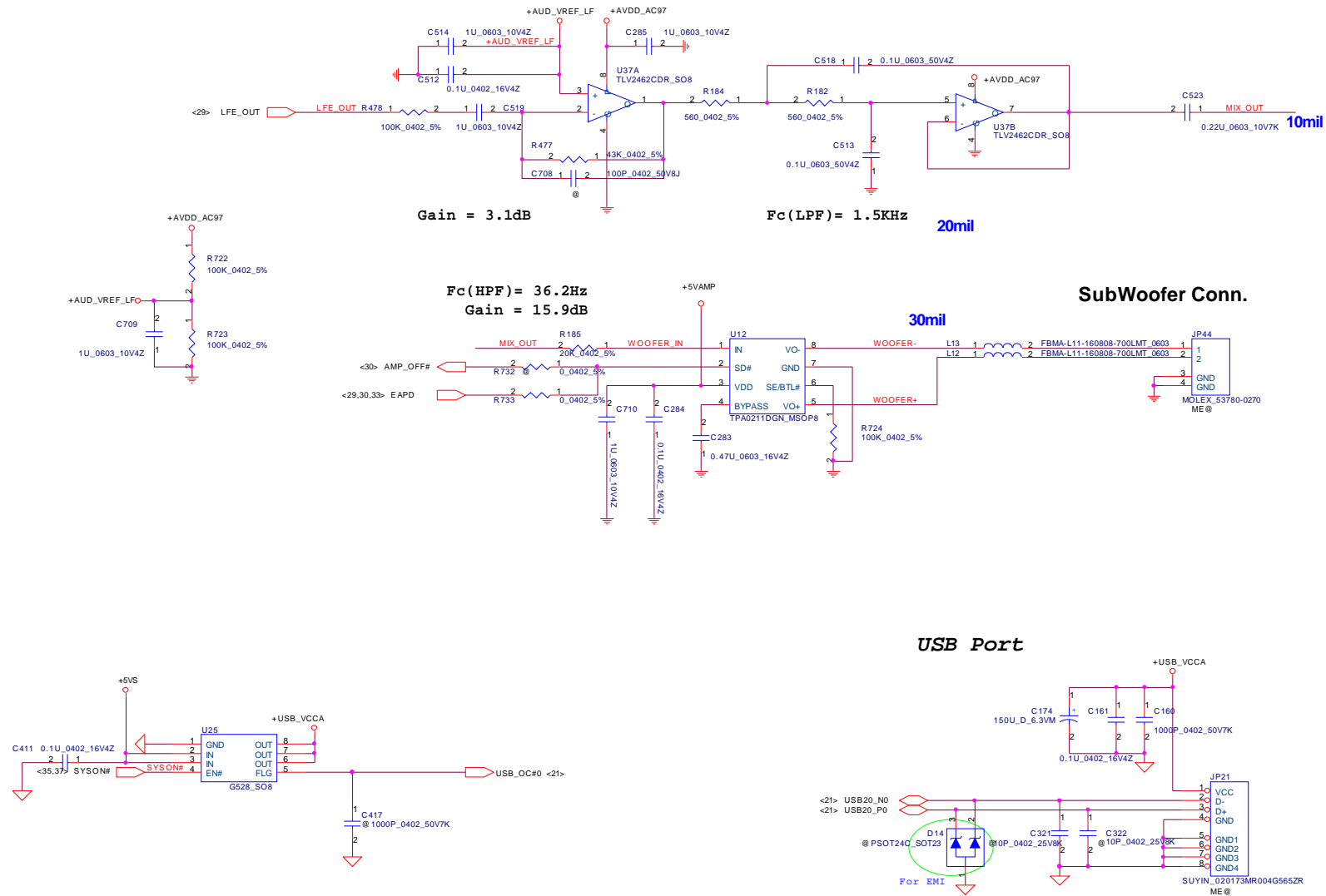
Mini-Express Card(Slot 1-WLAN)



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Size	Document Number	Rev		0.1
Custom	HGT30/31 LA3061	Date: 2006.03.23		Sheet 28 of 47

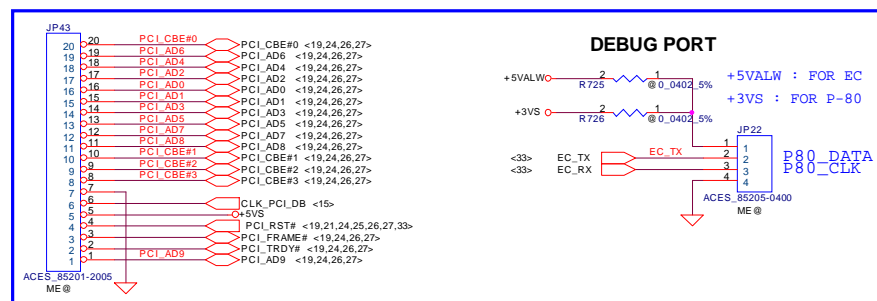
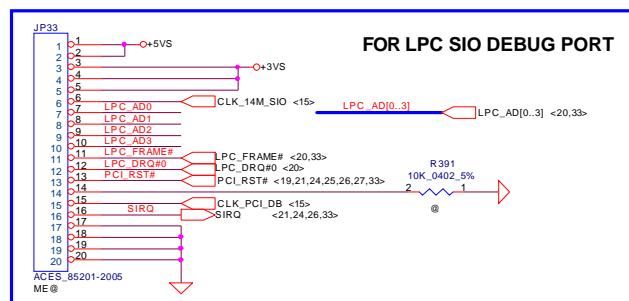
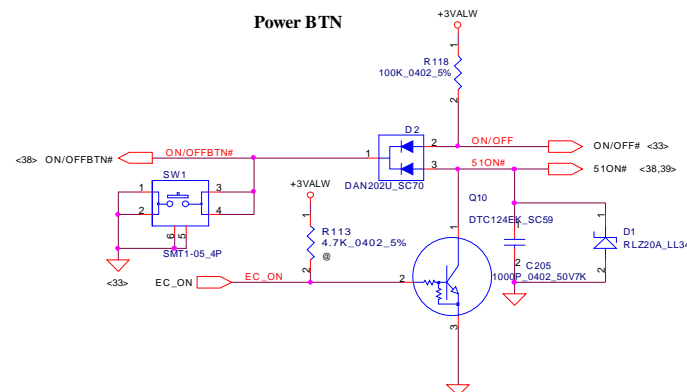


WOOFER@

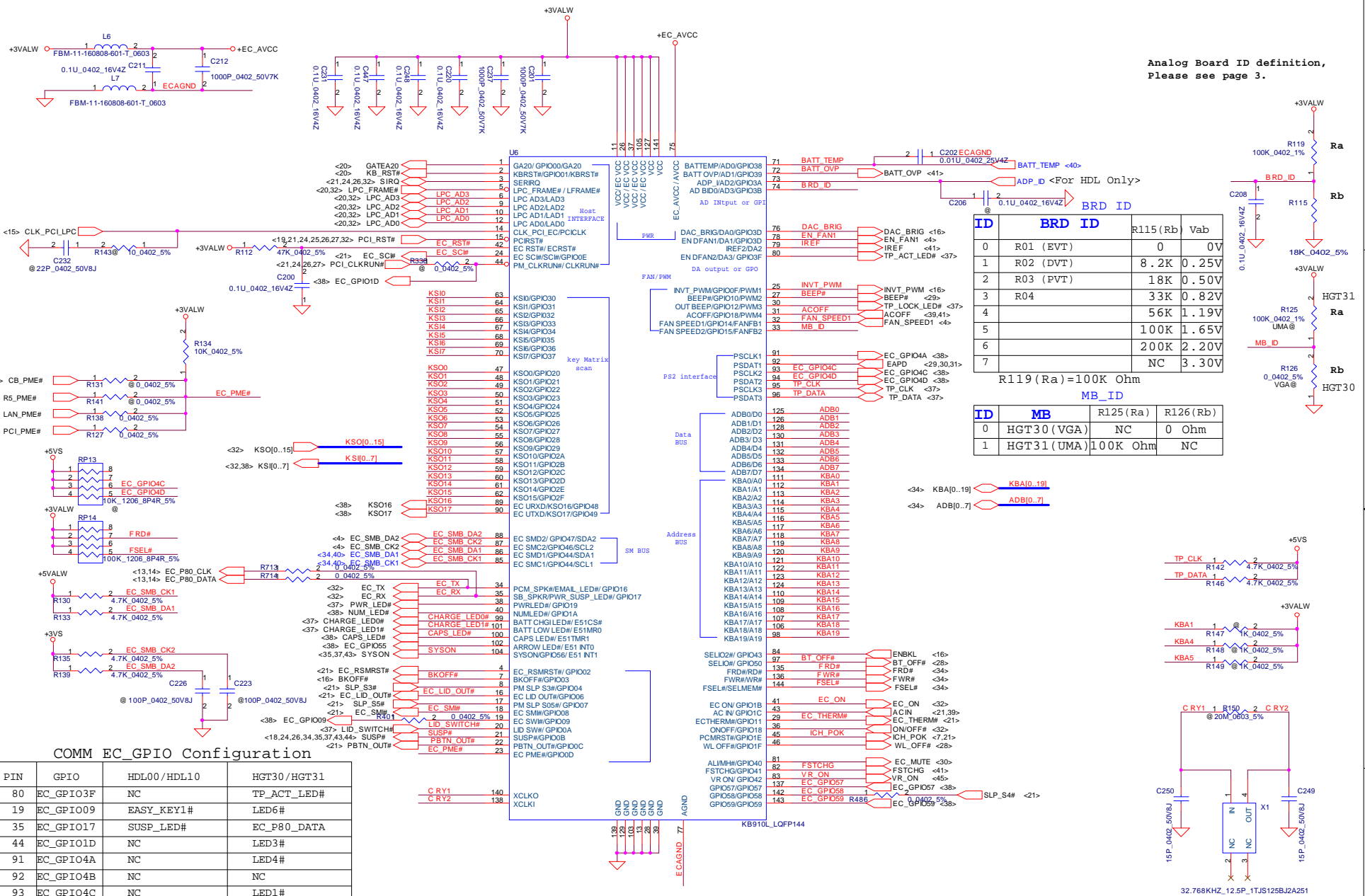


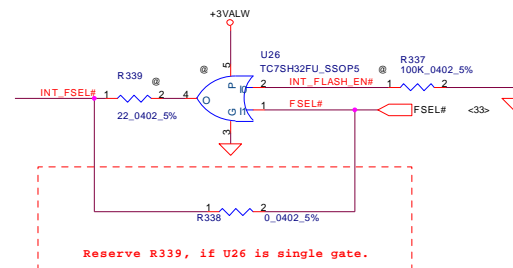
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Figure 10 illustrates a 240000-bit ACS code. The code is organized into 10000 groups, each containing 24 bits. Each group is represented by a small table with 4 rows (KSI0 to KSI5) and 6 columns (0 to 5). The groups are numbered 1 to 10000. The first group is labeled 'JP26' and the last group is labeled 'ACS. 85202-2405'. The groups are connected by a horizontal line, indicating a sequence of groups.

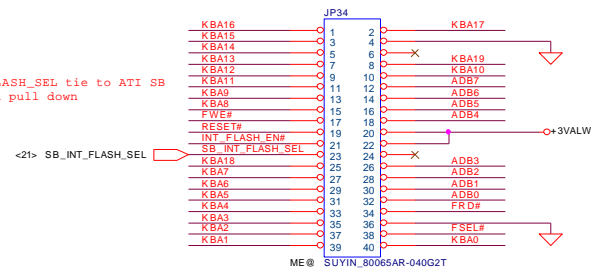


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Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	KBD,ON/OFF,T/P,LED/B,DEBUG Size Custom Document Number HGT30/31 LA3061
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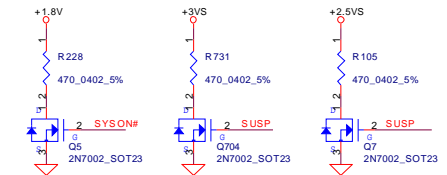
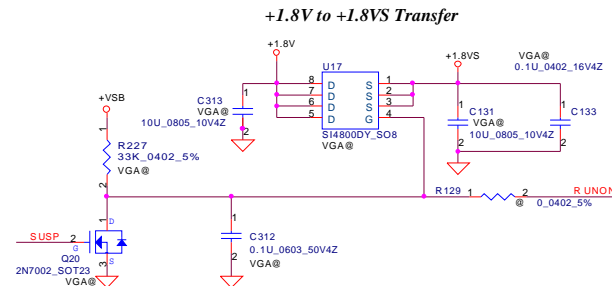
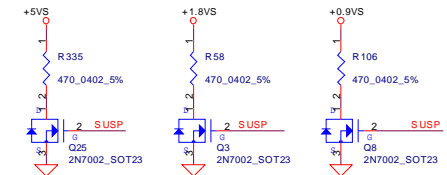
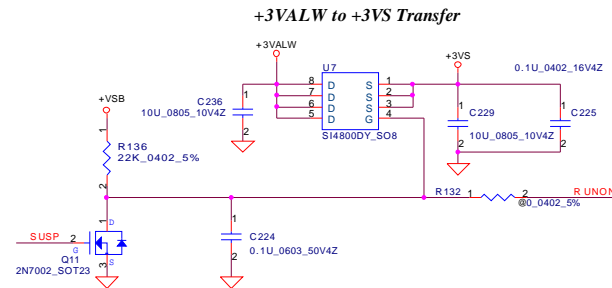
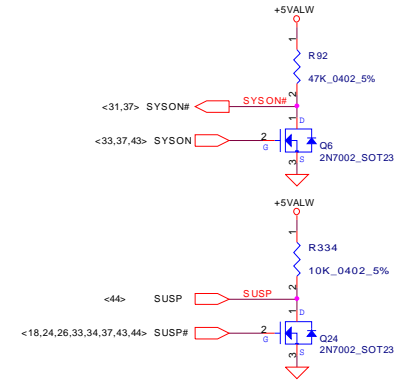
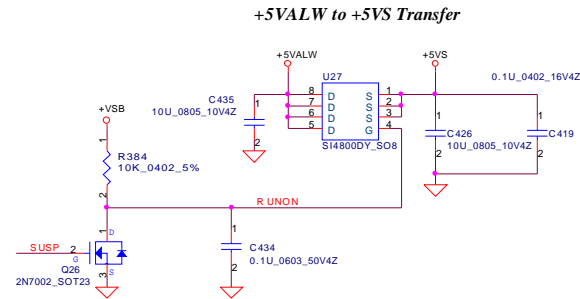




1MB Flash ROM



WWW.AliSaler.Com

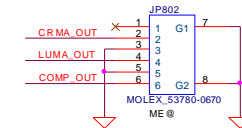


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Issued Date	2005/10/06	Deciphered Date	2006/10/06	DC/DC Circuit
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				HGT30/31 LA3061
				Rev 0.1
				Date: 星期二, 三月 23, 2006
				Sheet 35 of 47

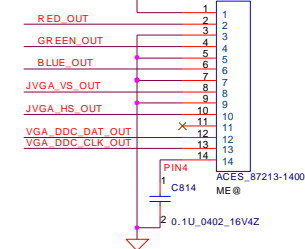
CLOSE TO JTVOUT1

VGA I/O PORT Connector

S-VIDEO



DSUB



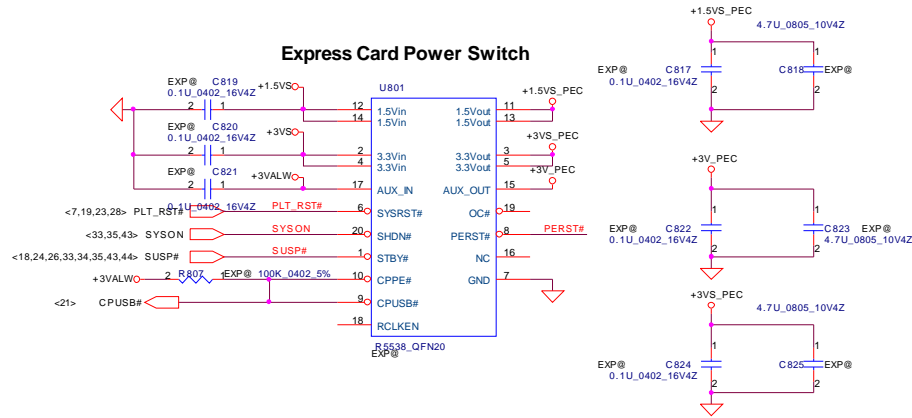
PIN ASSIGMENT

PIN	D-SUB	FUNCTION	PIN	SVIDEO	FUNCTION
1	9	+CRT_VCC	1	1	NC
2	1	RED	2	4	CRMA
3	6	GND	3	2	GND
4	2	GREEN	4	3	LUMA
5	7	GND	5	5	GND
6	3	BLUE	6	6	CVBS
7	8	GND			
8	14	VSYNC			
9	10	GND			
10	13	HSYNC			
11	11	SENSE			
12	12	SM_DAT			
13	15	SM_CLK			
14	4	PIN4			

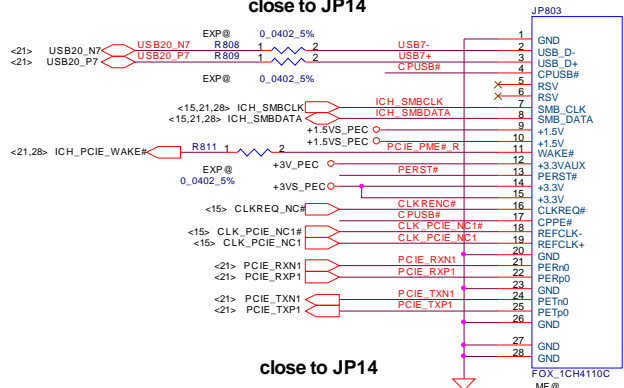
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Issued Date	2005/03/10	Deciphered Date	2006/03/10	Size	Document Number
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				Date	Rev 0.1
				Sheet	36 of 47

NEW CARD FOR C38

Express Card Power Switch

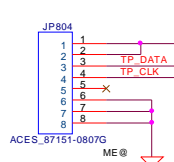


close to JP14

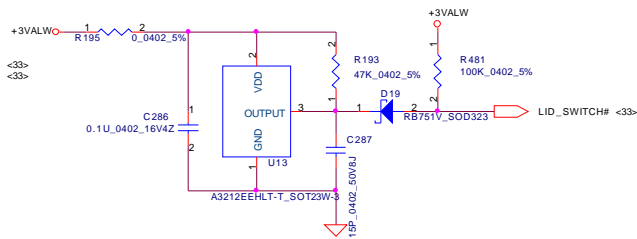


close to JP14

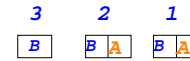
T/P Board



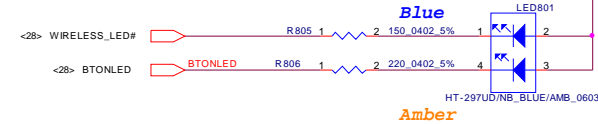
LID Switch



LED Indicator ON M/B



Wireless / Bluetooth LED

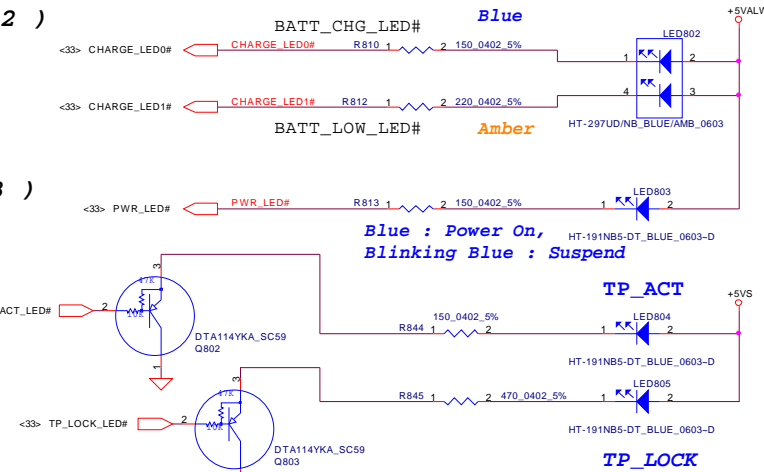


STATUS	LED Color
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

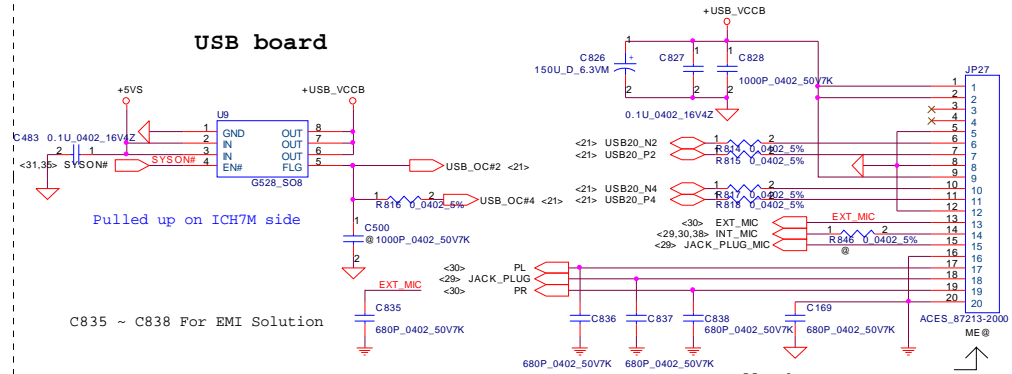
(1)

(2)

(3)



USB board



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						Size		Document Number		Rev	
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[illegible][illegible][illegible]

RJ11+RJ45 CONN

SWITCH BD

LED Indicator BD

1	2	3	4	5	6
L		M			H

Function

KEY Matrix	K016	K017
K010	DW-UP	DW-DOWN
K011	DW-ENTER	MUTE

Dial Wheel

NOVA_BTN#

WIRE_LAN_BTN#

HDD

CD-ROM

CAPS_LED#

NUM_LED#

NOVO BTN

MDC CONN

NOVA_BTN#

WIRE_LAN_BTN#

HDD

CD-ROM

CAPS_LED#

NUM_LED#

NOVO BTN

MDC CONN

1	2	3	4	5	6
L		M		H	

Function		
KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

NOVO_BTN# 1 2 EC_GPIO57 EC_GPIO57 <33>
3 51ON# 51ON# <32,39>
D810
DAN202U_SC70

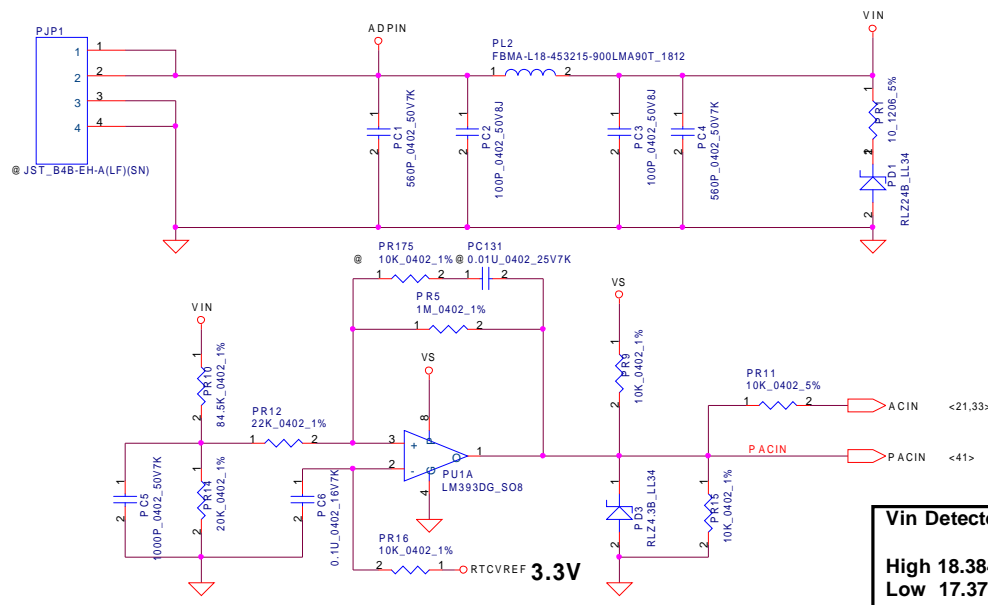
Diagram illustrating the RJ45 pinout for the EDL71 module. The RJ45 connector has two pairs of pins: pins 1 and 2 are labeled RJ_TIP and RJ_RING respectively, and pins 3 and 6 are labeled EDL71_MDC.

```
LED6#
LED5#
LED4#
LED3#
LED2#
LED1#
```

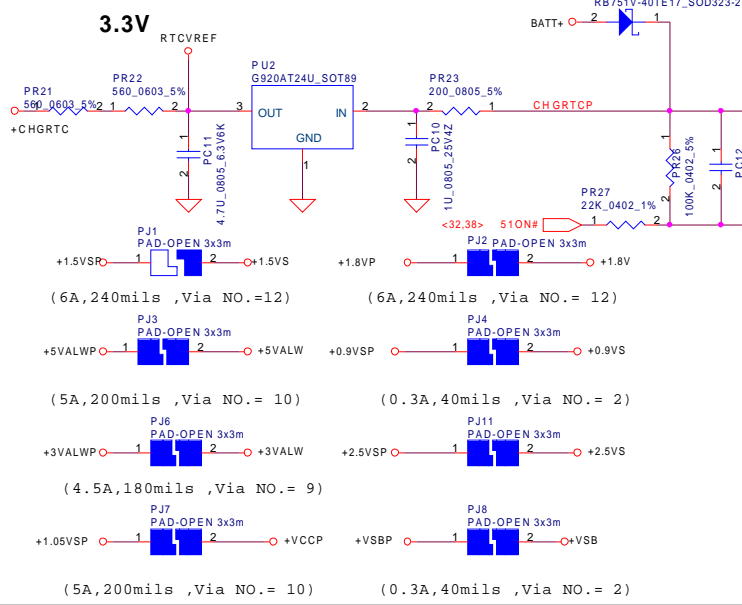
[illegible]

C839, C841, C842 For EMI Solution

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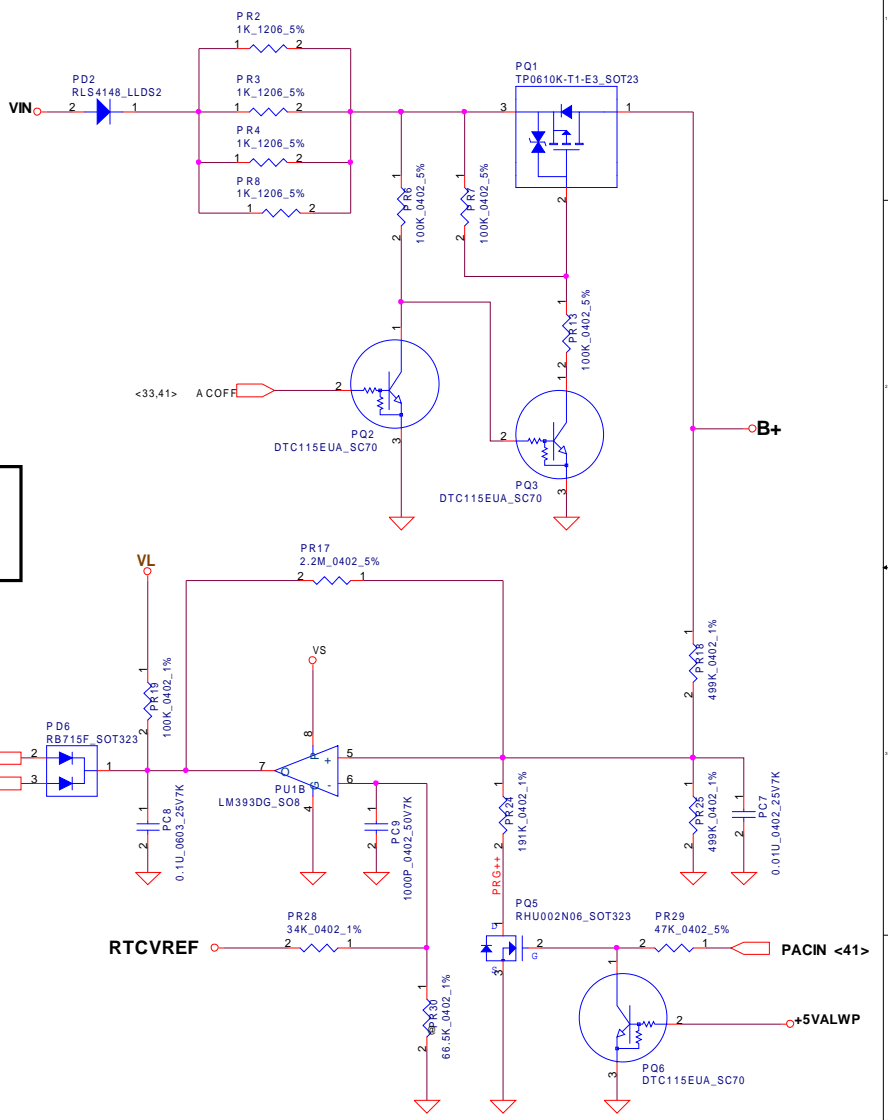


Vin Detector		
High	18.384	17.901
Low	17.370	16.907
	17.430	16.630

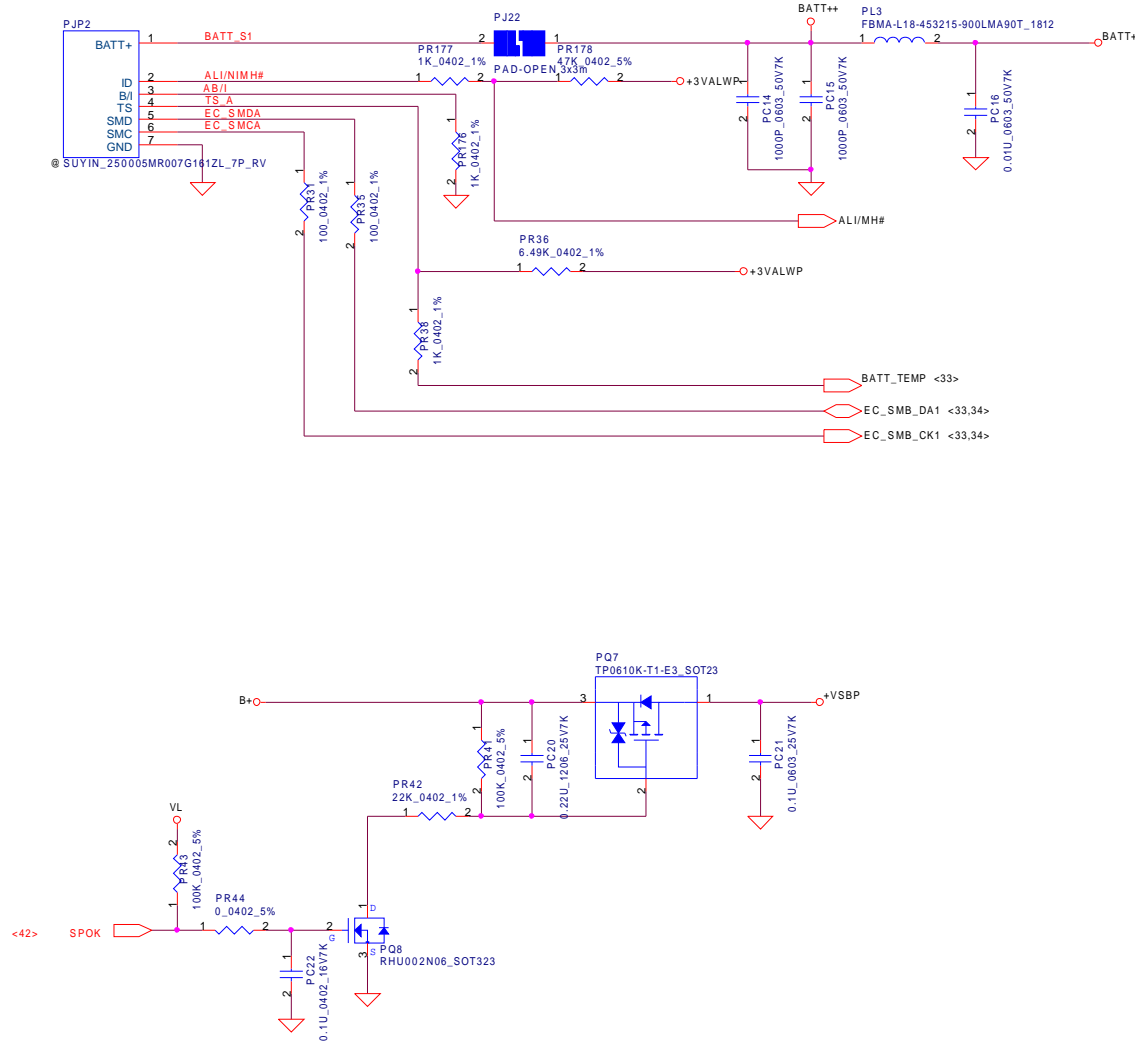


ACIN			
Precharge detector			
	Min.	typ.	Max.
H-->L	14.620V	14.853V	15.245V
L-->H	15.534V	15.970V	16.421V

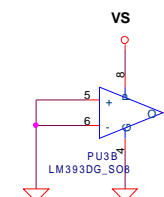
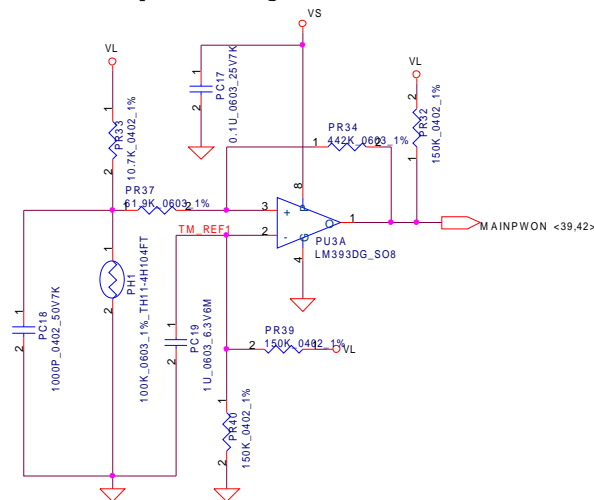
BATT ONLY			
Precharge detector			
	Min.	typ.	Max.
H-->L	6.169V	6.231V	6.361V
L-->H	7.168V	7.349V	7.537V



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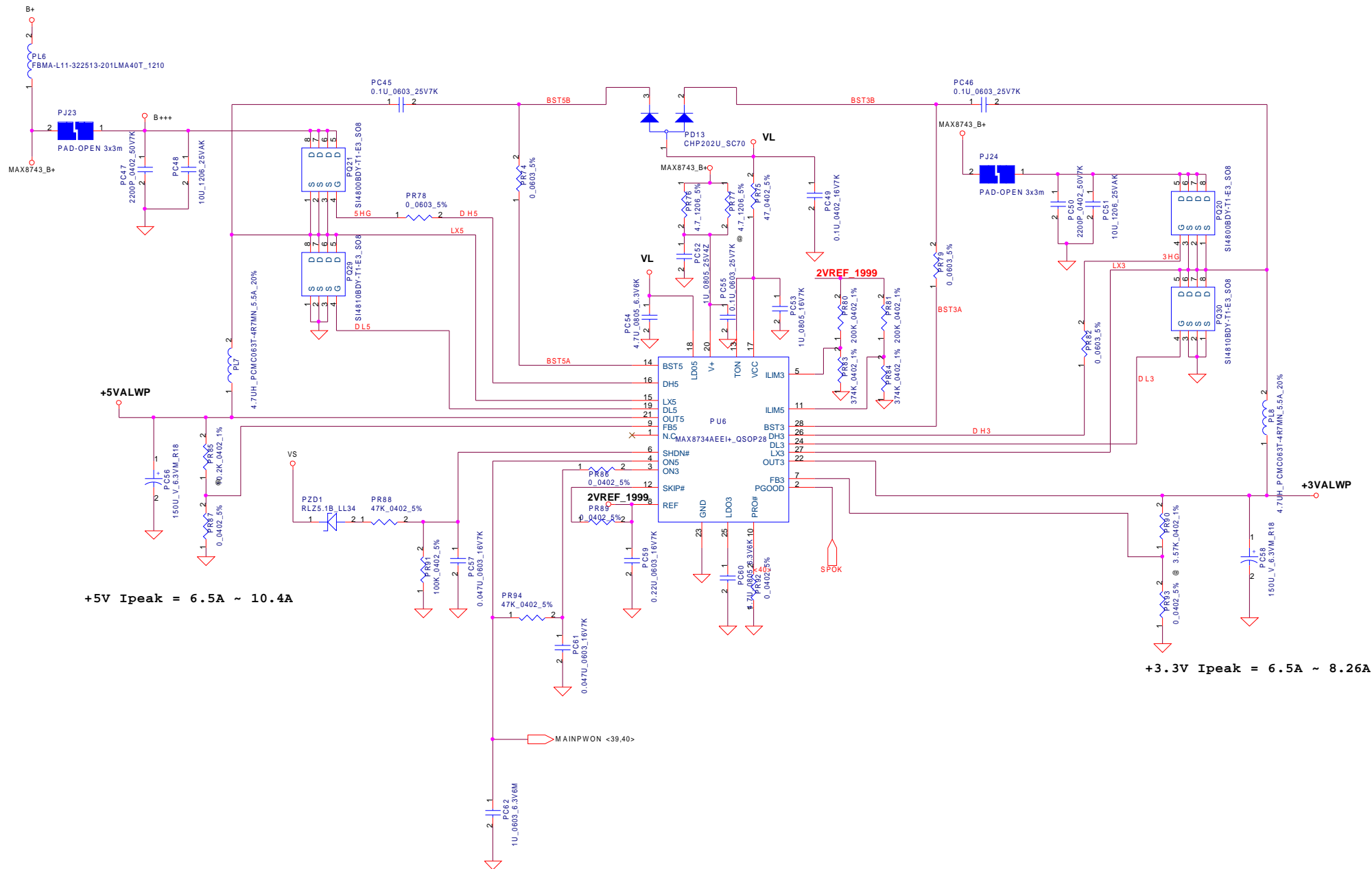
PH1 under CPU bottom side :
CPU thermal protection at 85 degree C
Recovery at 70 degree C



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					Size	Document Number	Rev
					B		0.1
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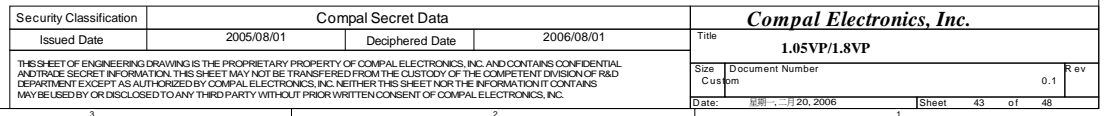
$$F_{osc} = 14100 / R_t = 14100 / 47 = 300 \text{ KHz}$$

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						Size B	Document Number			Rev 0.1
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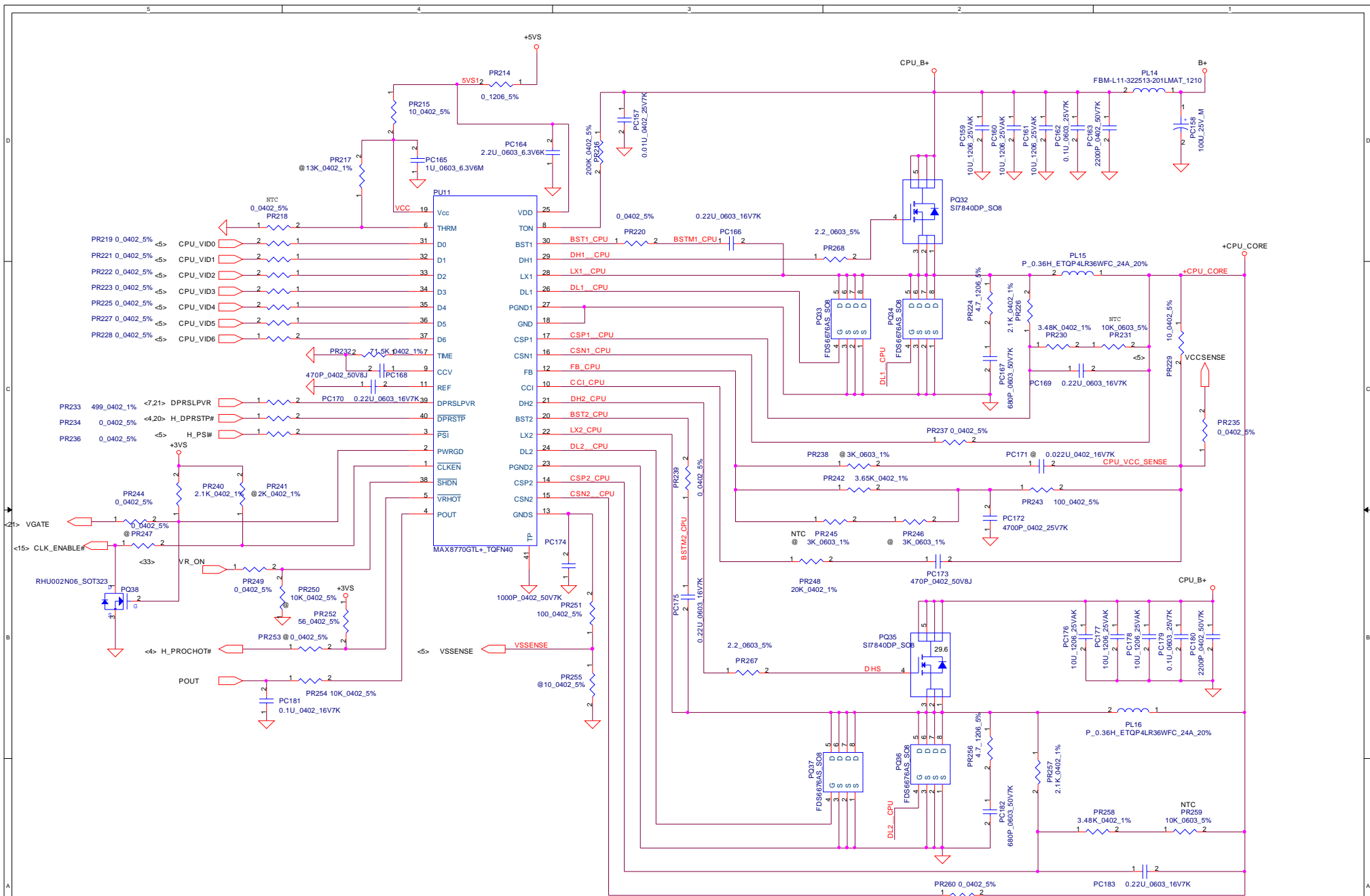


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+1.05VSP Ilimit=6.33A~10.03A







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Cust	Form	2006/06/20		45	0.1

Version change list (P.I.R. List)

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for PWR

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1		MODIFY 3V/5V current limit to 6.5A~8.1A/6.5A to 10.2A		42	MODIFY PR83/PR84 FROM 499K TO 374K		DVT
2		ADD or decrease CPU CORE ring with EMI solution : snubber		45	Reserve PR224//PR256: 4.7 1206 ,add PC167/PC182:680P		DVT
3		Reserve PR267,PR268 seperate in CPU CORE high side gate for EMI require		45	Reserve PR267,PR268:0 0603		DVT
4		change PJP1 from 5 pin to 4 pin		39	change PJP1 from 5 pin to 4 pin		DVT
5		modify sequecce		43	change PR179 to 100k, PC132 =0.1U		DVT
6		modify Vgate		45	add PQ38:RHU002N06,PR240:2K,delete PR247		
7							
8							
9							
10							
11							
8							
9							

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Version change list (P.I.R. List)

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I tem	Fixed Issue	Rev.	PG#	Modify List	B.Ver#	Phase
1	Add EC_Port80 Signals to DDR2 DIMM1 & DIMM2	0.2	P.13 P.14	1.Connect U6.91 (EC_P80_CLK) through a 0 Ohm R to JP3.83 & JP4.83 2.Connect U6.92 (EC_P80_DATA) through a 0 Ohm R to JP3.69 & JP4.69	0.2	DVT
2	Impedance not match for both CRT & TV Out	0.2	P.17	R3, R4, R5, R6, R7, R8 Change from NC to 150 Ohm	0.2	DVT
3	LCD Panel will flash white frame when power on	0.2	P.16	no stuff D10 (CH751H), stuff R204 (100K)	0.2	DVT
4	ICH7's GPIO configuration modification	0.2	P.21	GPIO10 connect to ACIN GPIO7 connect to G7X_THER_ALERT# GPIO39 connect to KILL_MDC#	0.2	DVT
5	<New Add> MDC supports S4/S5 resuming	0.2	P.28	MDC power connection change to +3VALW from +3VS	0.2	DVT
6	Audio Circuit modification for : 1. Line-Out connection change from Pin.35/Pin.36 to Pin.43/Pin.45 2. Audio-OUT Auto-Switch by HP Plugging In 3. MicPhone Noise Reduction 4. Cleared off BO Sound from both entry of Windows XP & Power Off	0.2	P.29-31	Line-Out connection change from Pin.35/Pin.36 to Pin.43/Pin.45 Connect Pin.32 (LFE_OUT) through 2 luF Cap to Pin.43/Pin.45 +Audio_VREF_LF connection change to 1/2 +AVDD_AC97 Int.MIC connection changes to Pin31/Pin32 through luF for each New Add a JACK_PLUG_MIC signal from MIC JACK EAPD signal connect to EC'sGPIO4B APA2068's 13PIN(SE/BTL#) connect to GND	0.2	DVT
7	EC GPIO configuration modification	0.2	P.33	New add Port80 information OUT from Pin34(CLK), pin35(DATA) LED4 connection changes from GPIO17(35 PIN) to GPIO4A(91 PIN) New Add DAC's EAPD connect to EC's GPIO4B (92PIN). SKU_ID (GPIO3B) Changes to BRD_ID New Add WL_OFF# (GPIO1F, 46PIN) New Add BT_OFF# (GPIO50, 84PIN) New Add TP_ACT_LED# (GPIO3F, 80PIN) New Add TP_LOCK_LED# (GPIO12, 30PN)	0.2	DVT
8	Lid Switch changes from USB BD to M/B	0.2	P.37	Circuit of Lid SW changes from USB BD to M/B (DEL Lid SW on USB BD)	0.2	DVT
9	Blue LED too dark when active due to VF too High on blue LED	0.2	P.37-38	Changes LED power from +3VS(+3VALW) to +5VS(+5VALWS)	0.2	DVT
10	New Add LED Buffer for LED4, TP_LOCK_LED#, TP_ACT_LED#	0.2	P.37 P.38	TP_ACT_LED# connect to Q802.2, and Q802.1 to GND, then connect to LED804 through a 220 Ohm R TP_LOCK_LED# connect to Q803.2, and Q802.1 to GND, then connect to LED805 through a 220 Ohm R EC_GPIO4A connect to Q804.2, and Q804.1 to GND, then connect to R283 (220 Ohm)	0.2	DVT
11	leakage of electricit when System is running S3 mode	0.2		NC For R458, R402, R411, R128, R831, R829	0.2	DVT

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Version change list (P.I.R. List)

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Item	Fixed Issue	Rev.	PG#	Modify List	B.Ver#	Phase
12	GIGA LAN function failed for both Loopback or PXE on RTL8110SCL solution	0.3	P.27	1. Add a R734 (0 Ohm) resistor between +2.5V_LAN & VTCT for MAC 2. Removed all of the bypass Cap.(C398, C396, C418, C408) for MAC's TCT pins 3. Removed all of pulled down resistors (49.9 Ohm) & Cap.(0.01u)	0.3	PVT
13	Subwoofer still make POP Sound	0.3	P.31	1. Add 0 Ohm resistors, R732 (0 Ohm) connection between AMP_OFF# & U12.2 (SD#) for reserved 2. a 0 Ohm connection between EAPD & U12.2(SD#)	0.3	PVT
14	Mic Switch between Int. & Ext. be Failed	0.3	P.29	1. R717.1 Disconnect from U11.17 2. R717.1 connect to U11.16	0.3	PVT
15	Wrong parts	0.3	P.35	Change package of C434, C224 & C312 from 0402 to 0603	0.3	PVT
16	Add a discharging path of +3.3VS	0.3	P.35	1. Connect +3.3VS to R731.1 2. Connect R731.2 to Q704.1 3. Connect Q704.2 to NET : SUSP signal 4. Connect Q704.3 to GND	0.3	PVT

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